

Annex G

(informative)

Application reference model

This annex provides the application reference model for this part of ISO 10303 and is given in figures G.2 through G.43. The application reference model is a graphical representation of the structure and constraints of the application objects specified in clause 4. The graphical form of the application reference model is presented in IDEF1X. The application reference model is independent from any implementation method. The diagrams use the IDEF1X graphical notation [2].

Extensions to the IDEF1X notation are used within the ARM diagrams through the use of symbols to denote off-page connectors. The symbols for the off-page connectors and their usage are drawn from the EXPRESS-G graphical modeling language and have the same meaning. Figure G.1 illustrates how off-page connectors are used to link relationships on different pages.

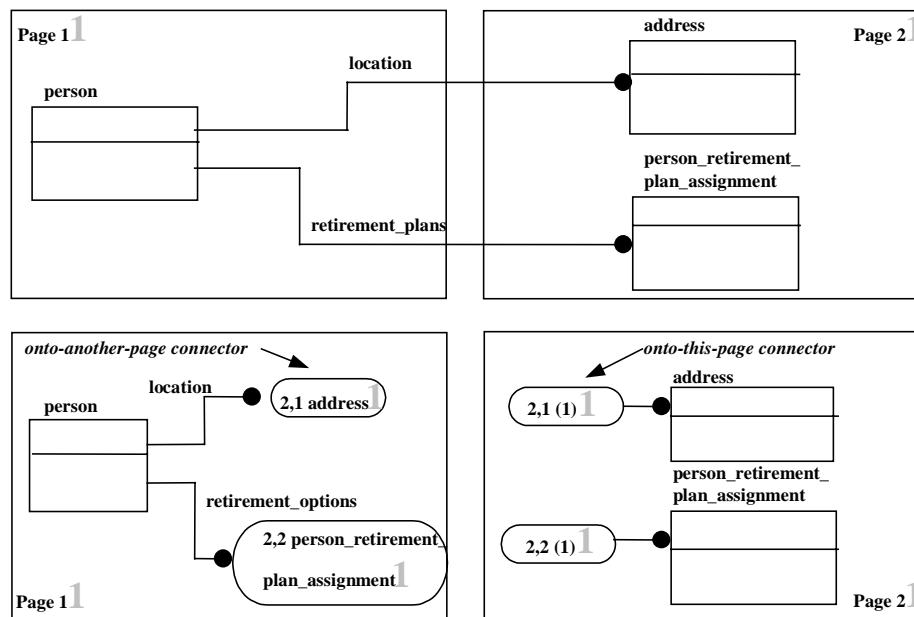


Figure G.1 - Off-page connectors

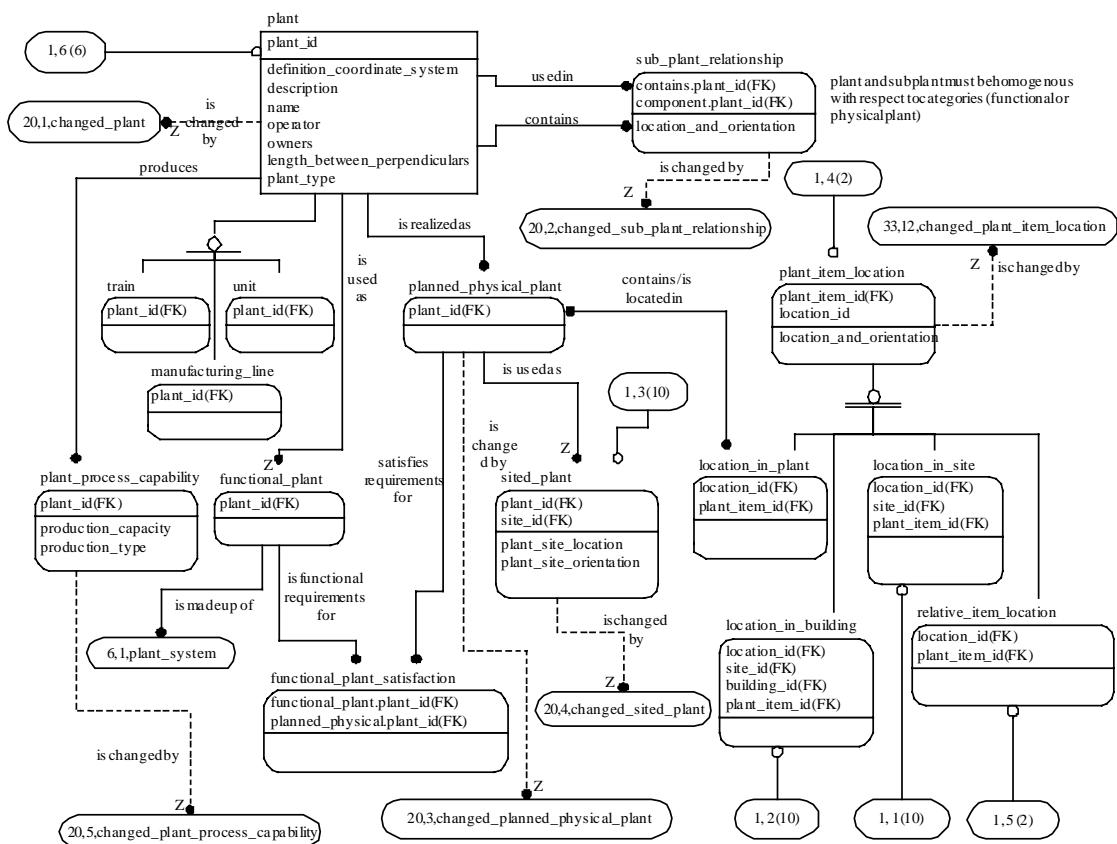


Figure G.2 - ARM diagram 1 of 42

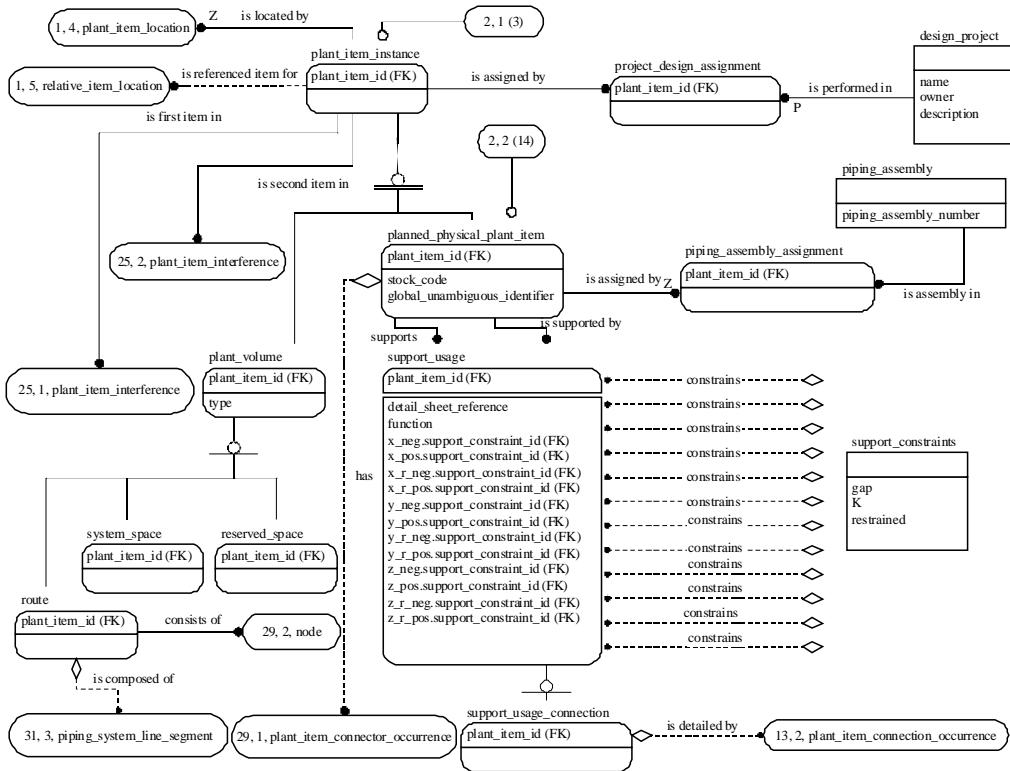


Figure G.3 - ARM diagram 2 of 42

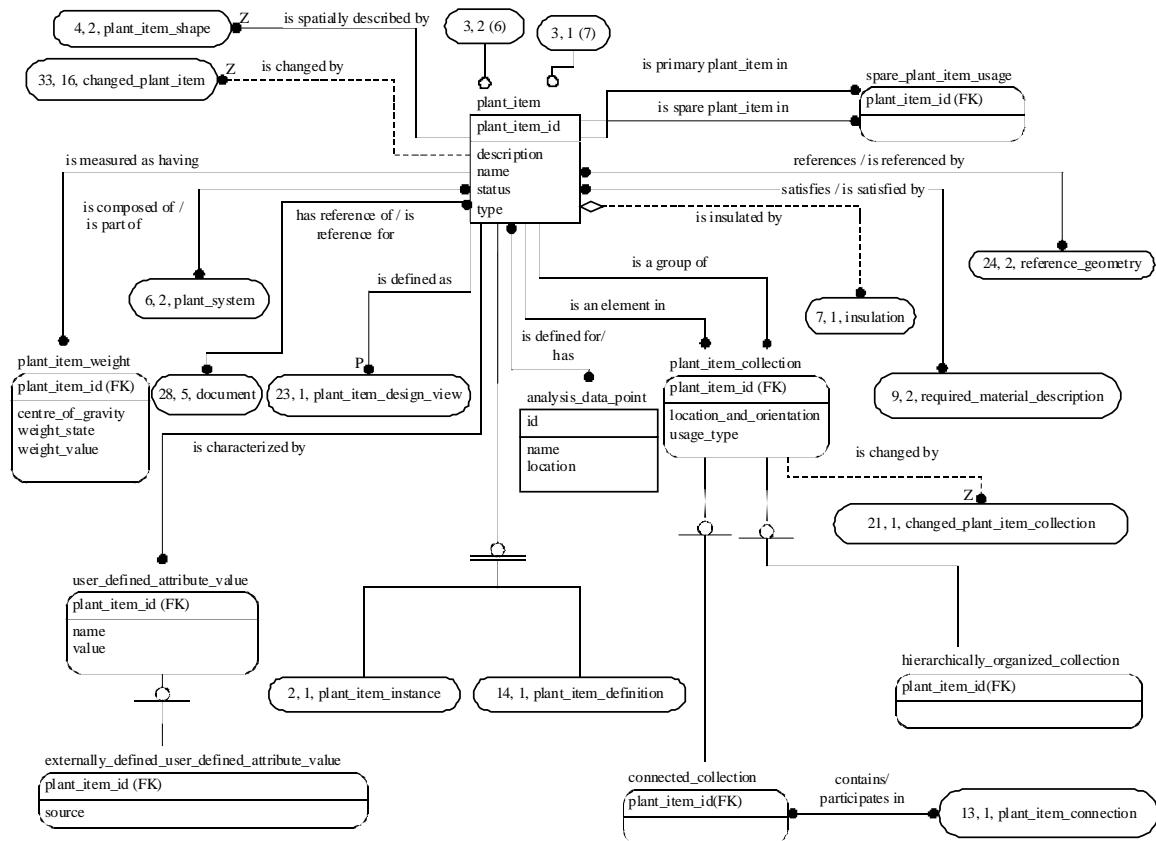


Figure G.4 - ARM diagram 3 of 42

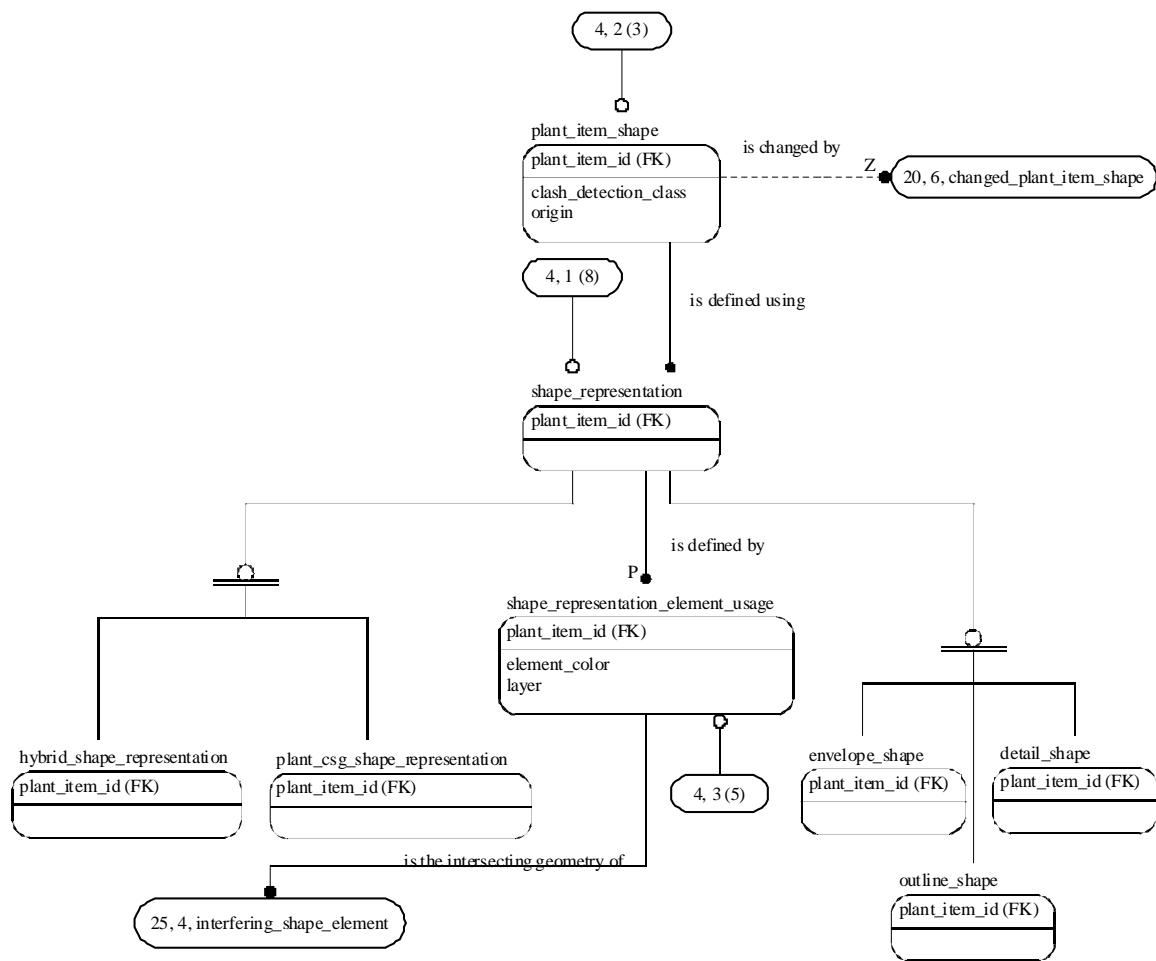


Figure G.5 - ARM diagram 4 of 42

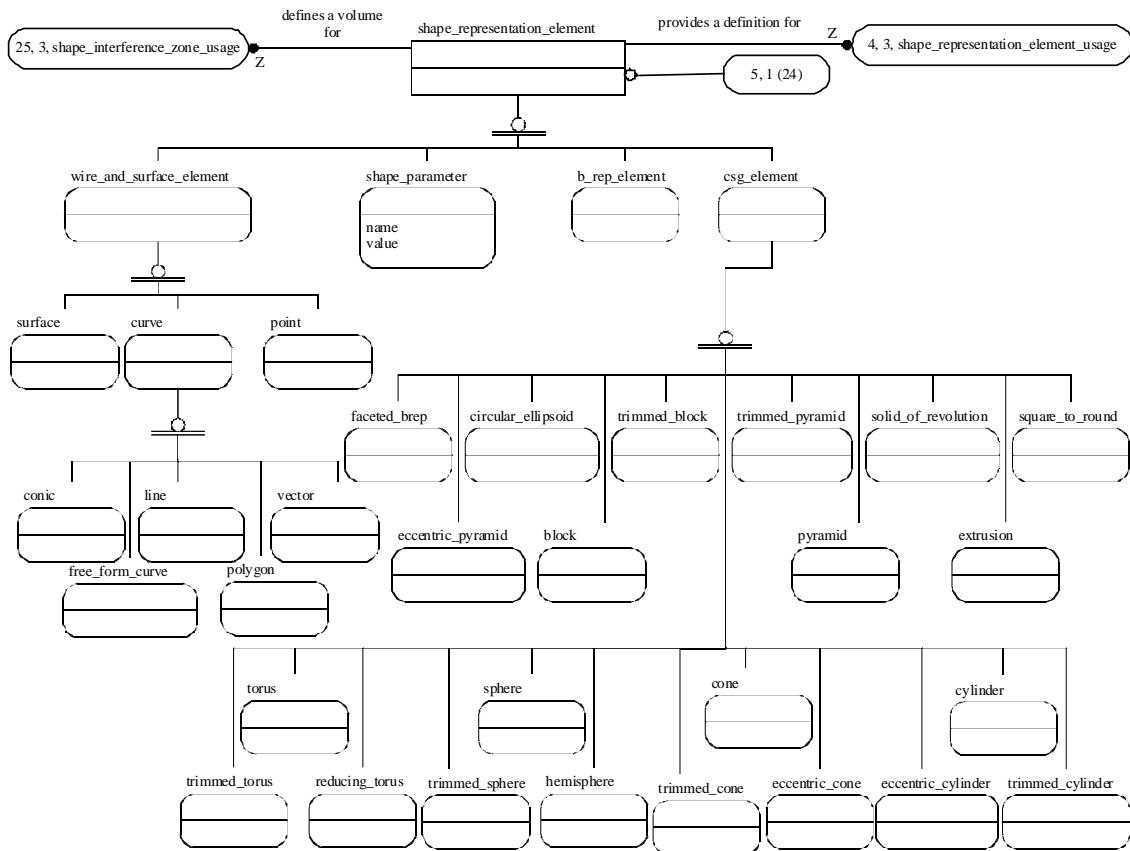


Figure G.6 - ARM diagram 5 of 42

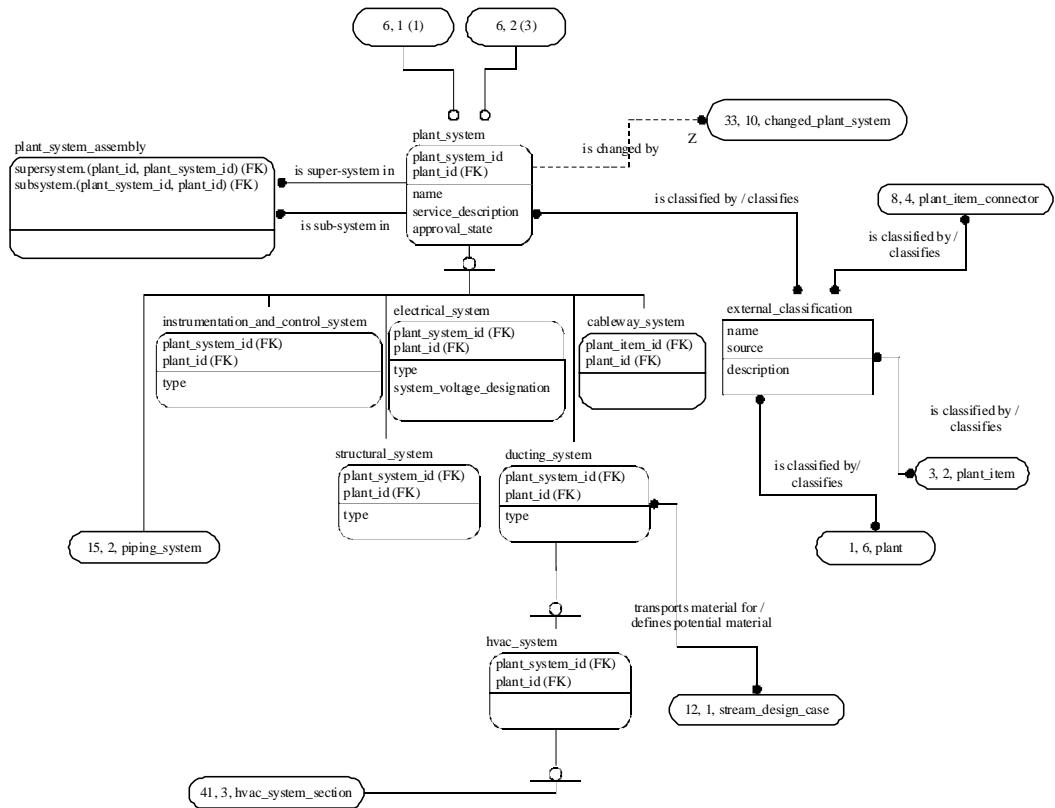
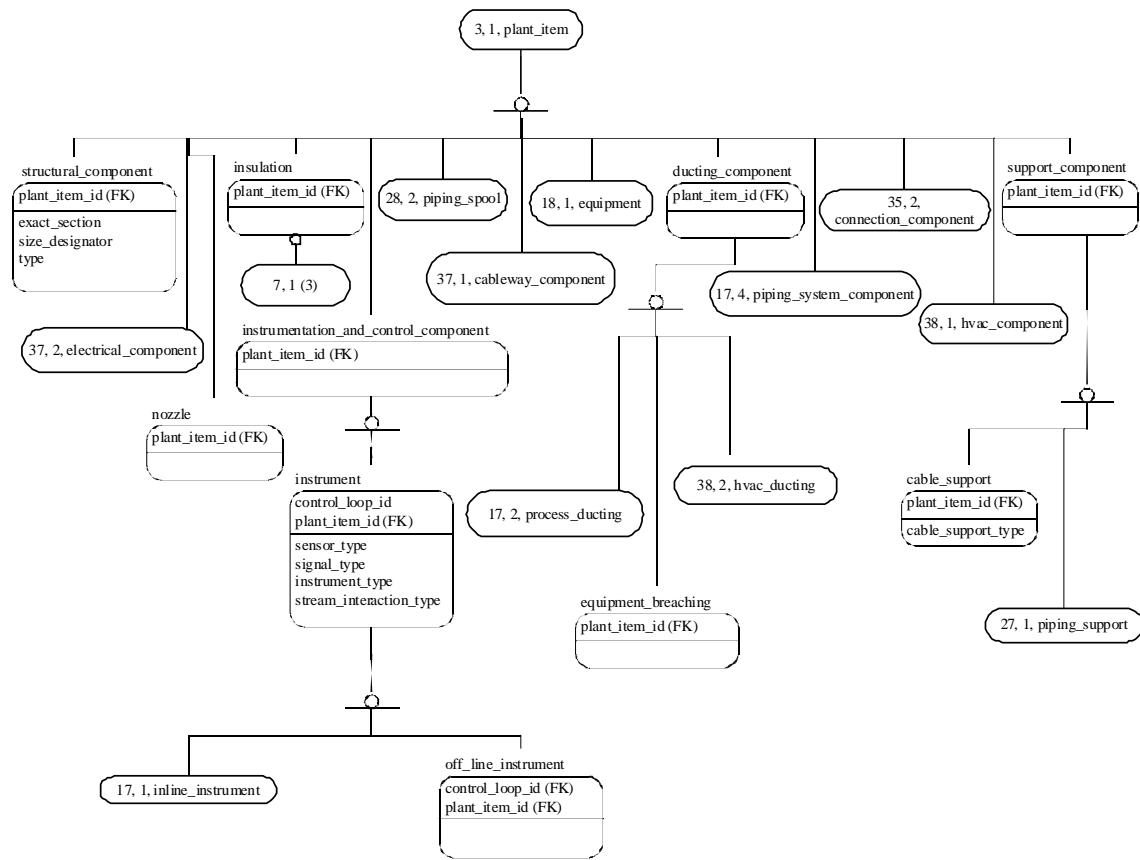


Figure G.7 - ARM diagram 6 of 42

**Figure G.8 - ARM diagram 7 of 42**

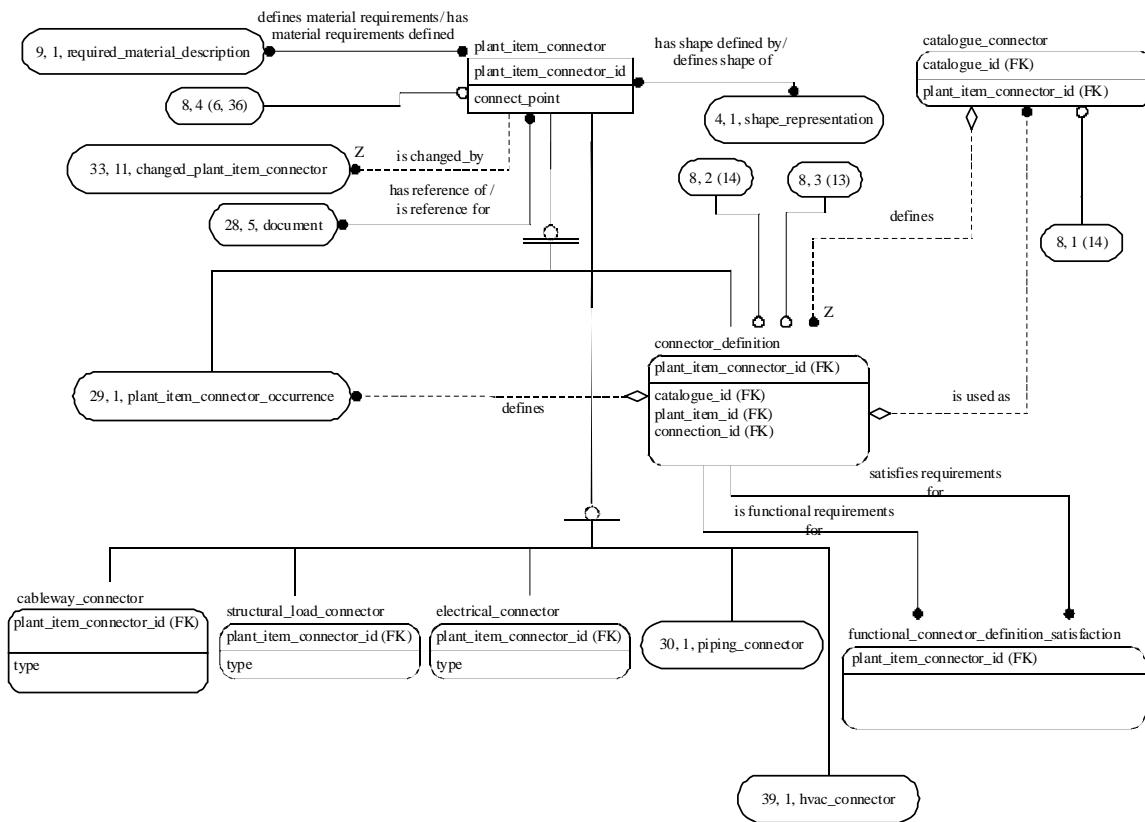


Figure G.9 - ARM diagram 8 of 42

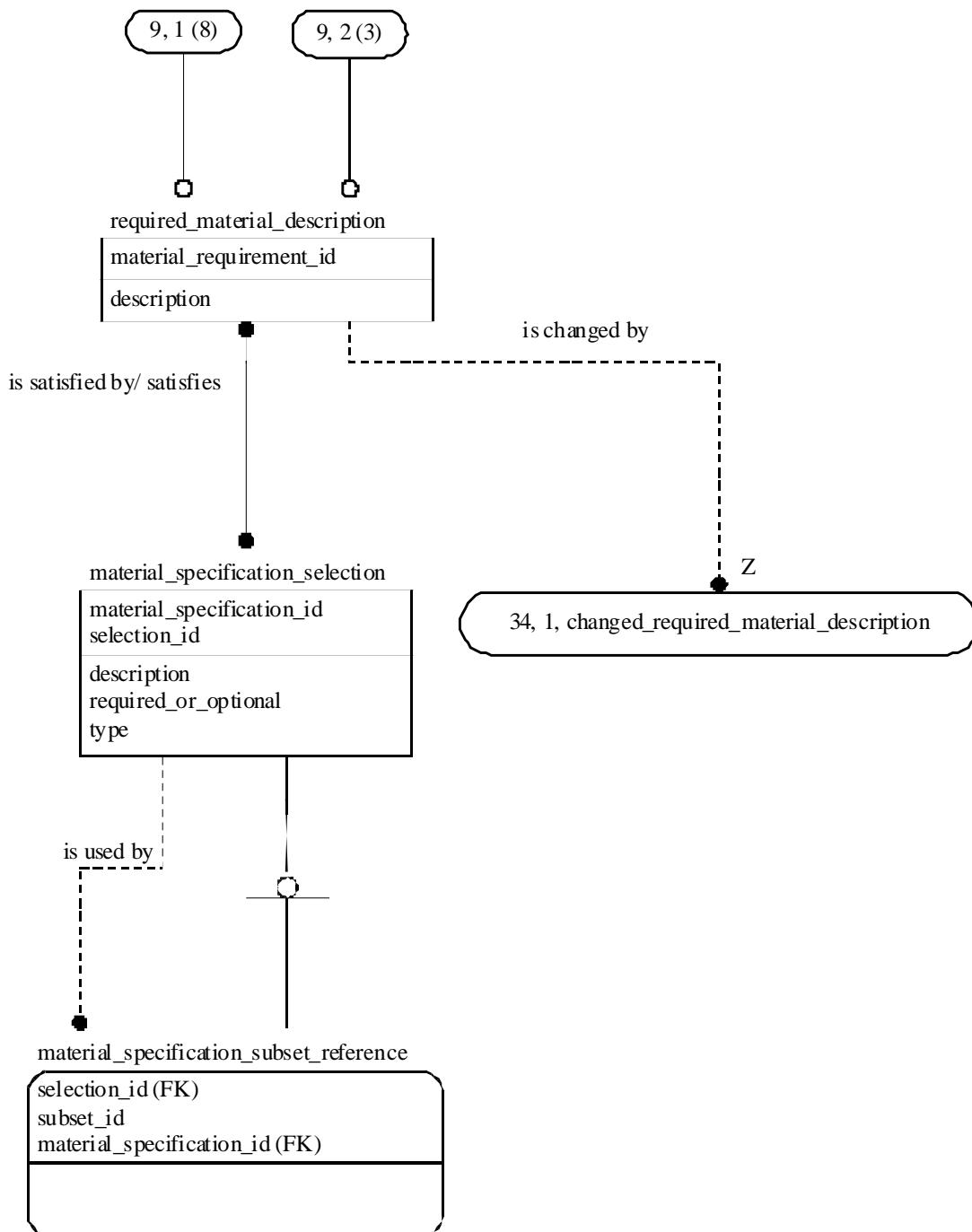


Figure G.10 - ARM diagram 9 of 42

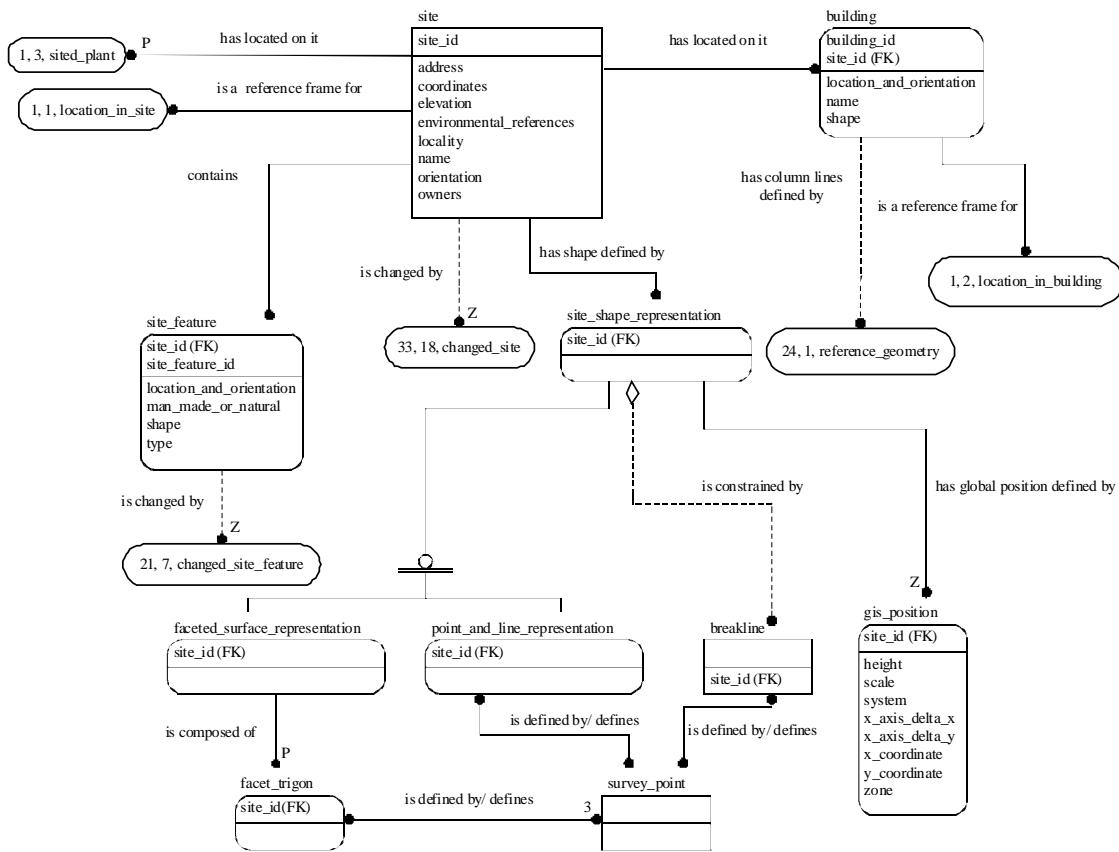


Figure G.11 - ARM diagram 10 of 42

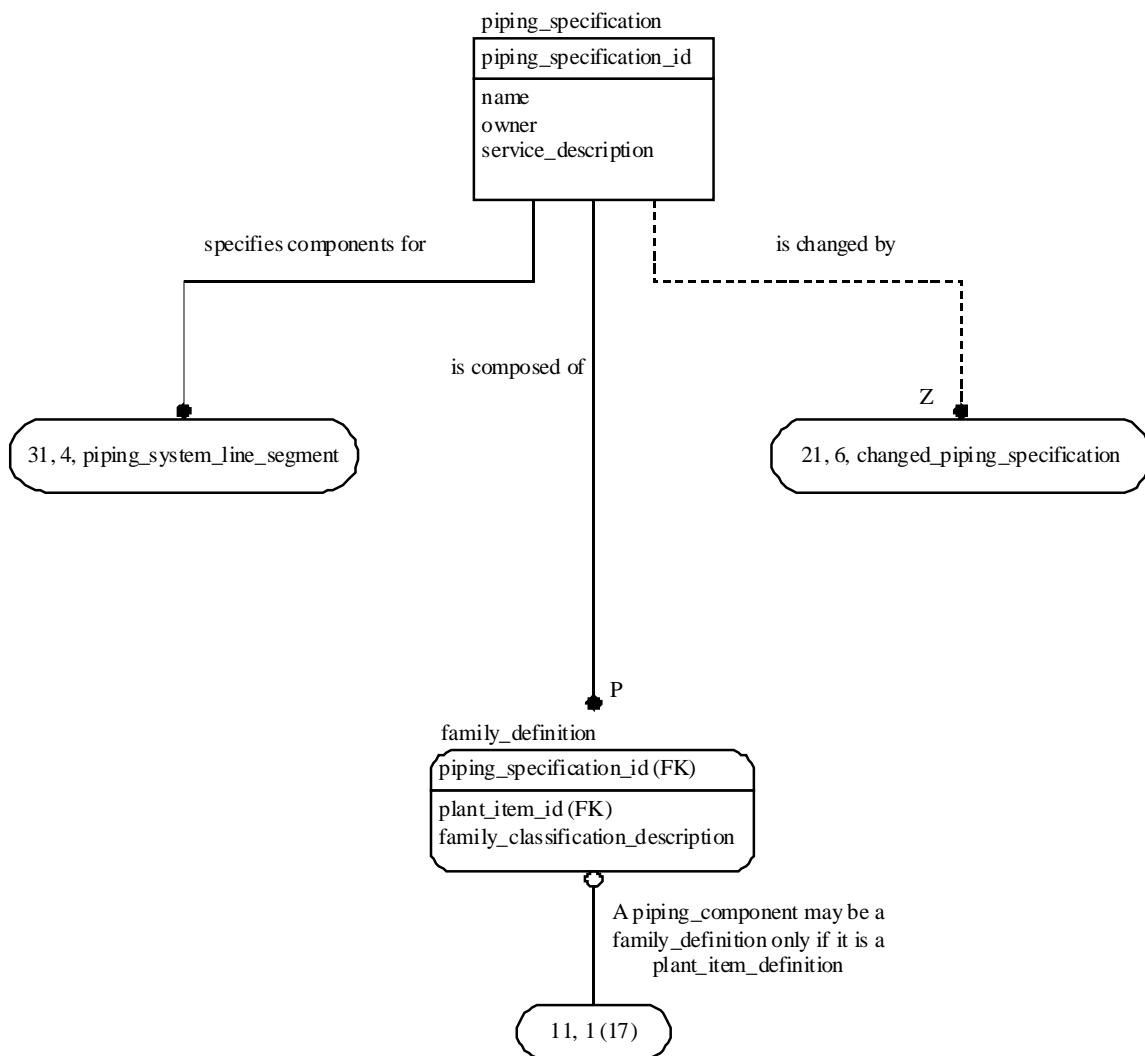


Figure G.12 - ARM diagram 11 of 42

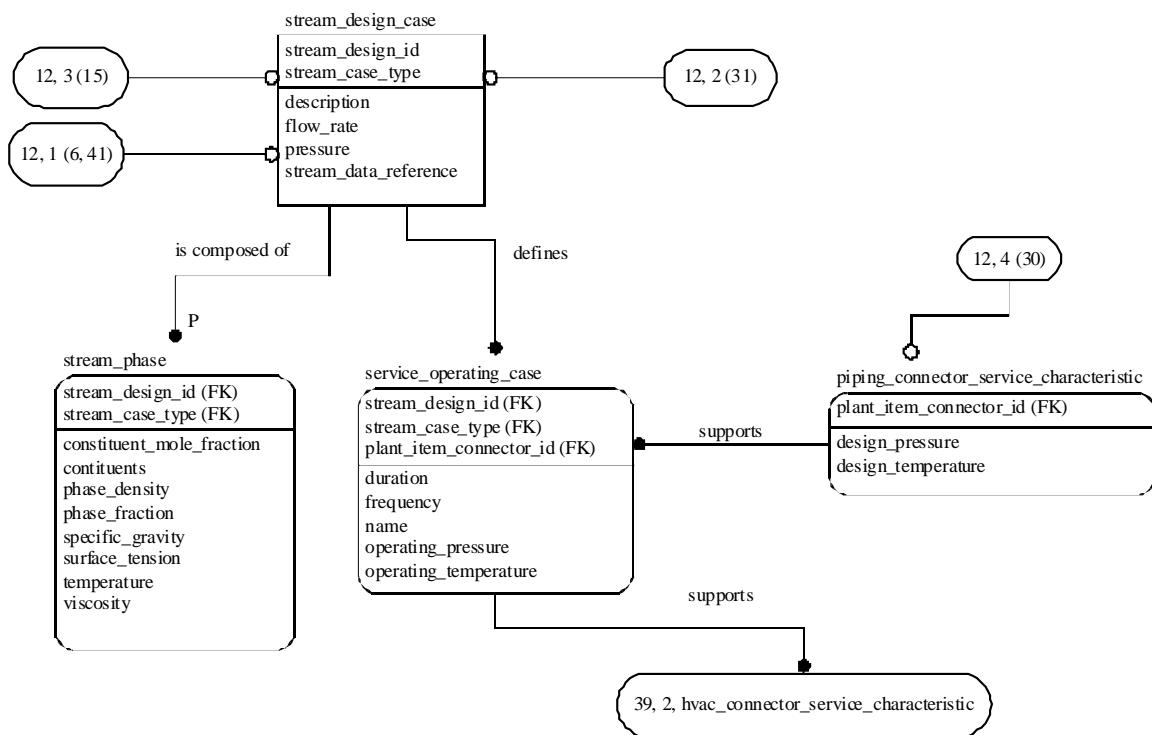


Figure G.13 - ARM diagram 12 of 42

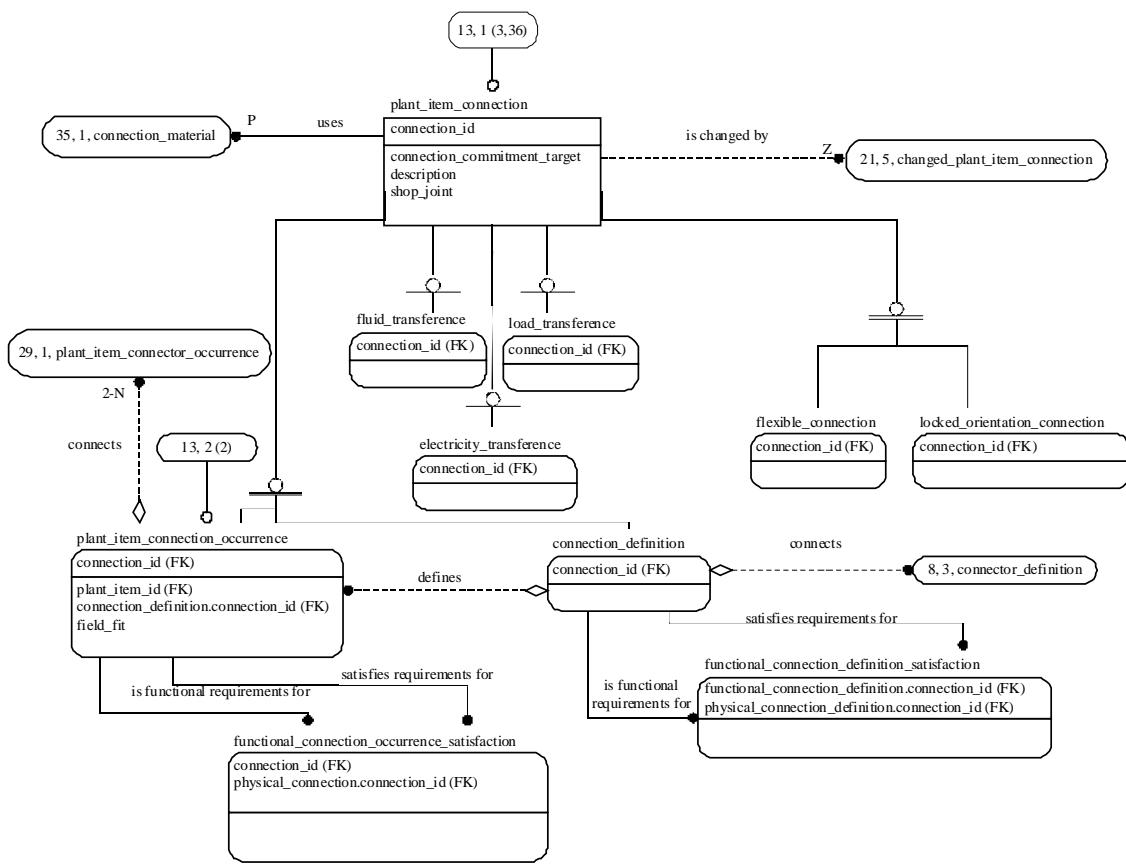


Figure G.14 - ARM diagram 13 of 42

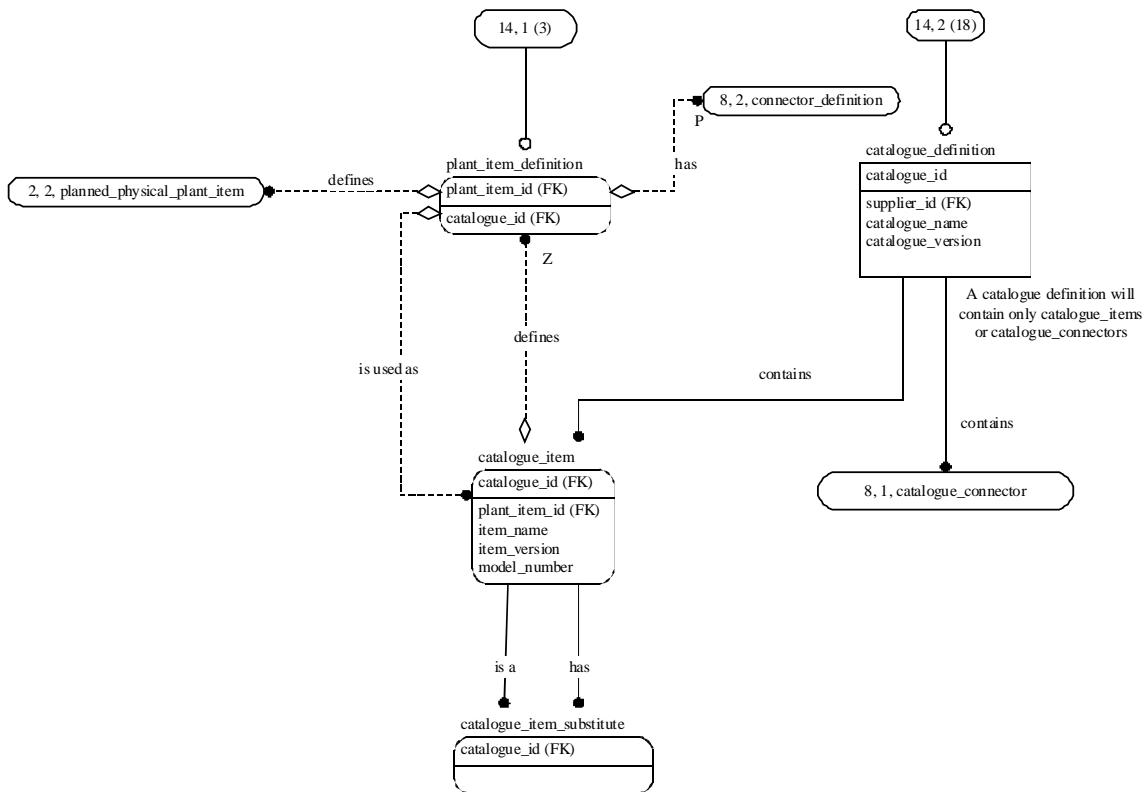


Figure G.15 - ARM diagram 14 of 42

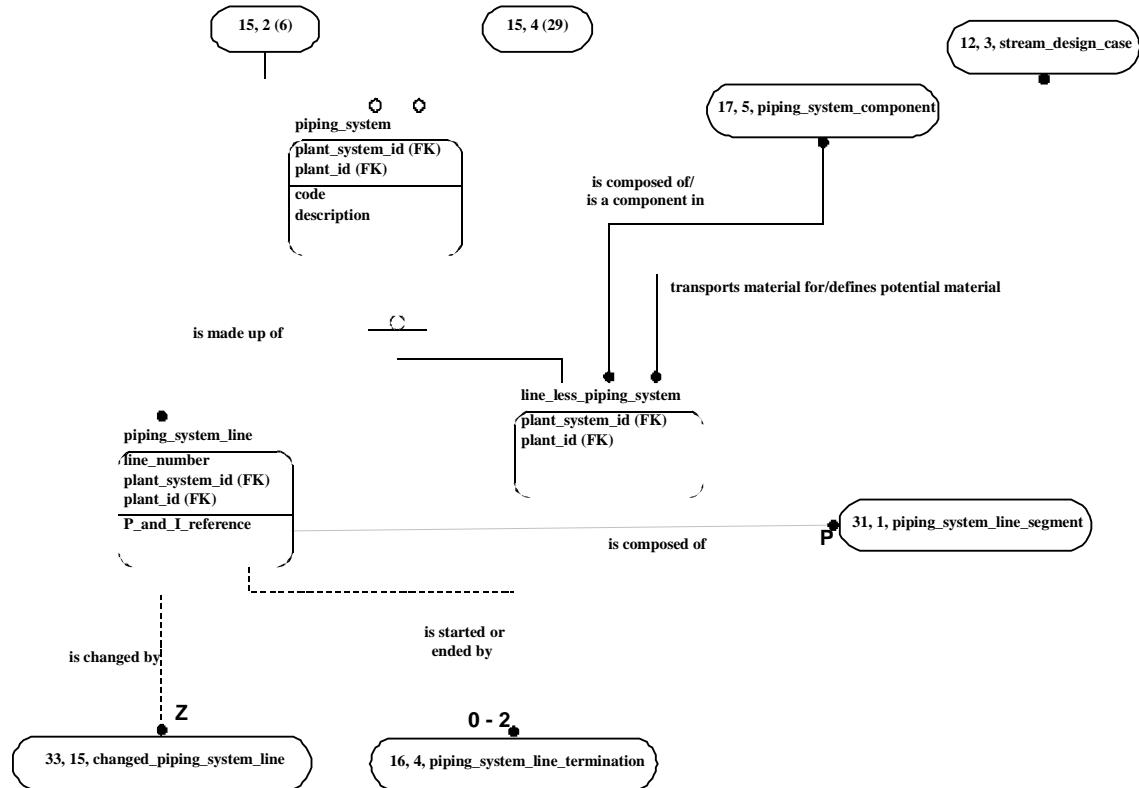


Figure G.16 - ARM diagram 15 of 42

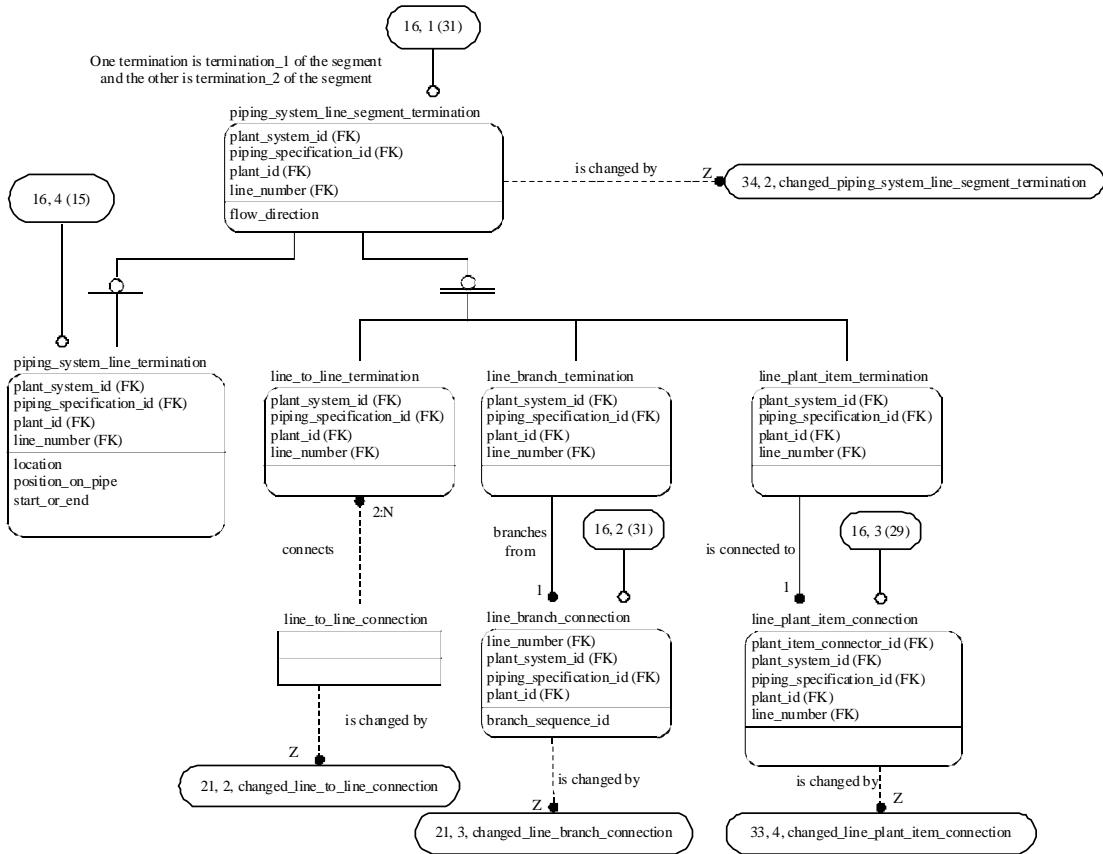


Figure G.17 - ARM diagram 16 of 42

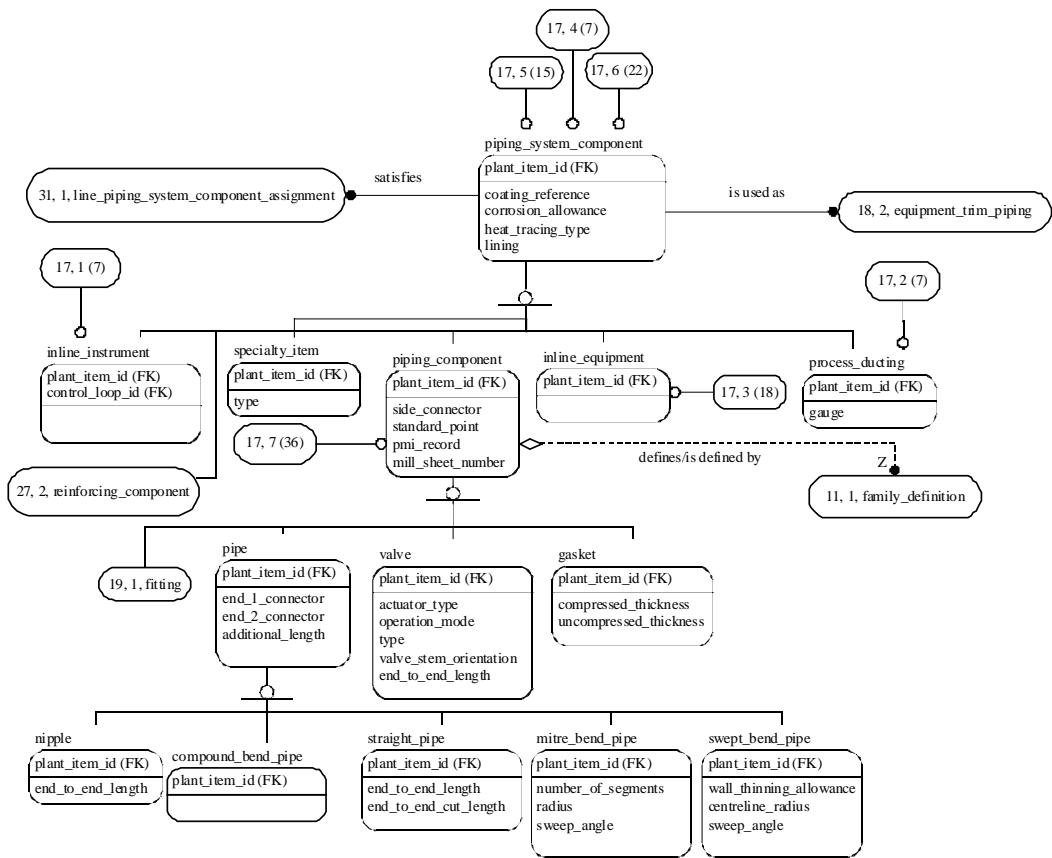


Figure G.18 - ARM diagram 17 of 42

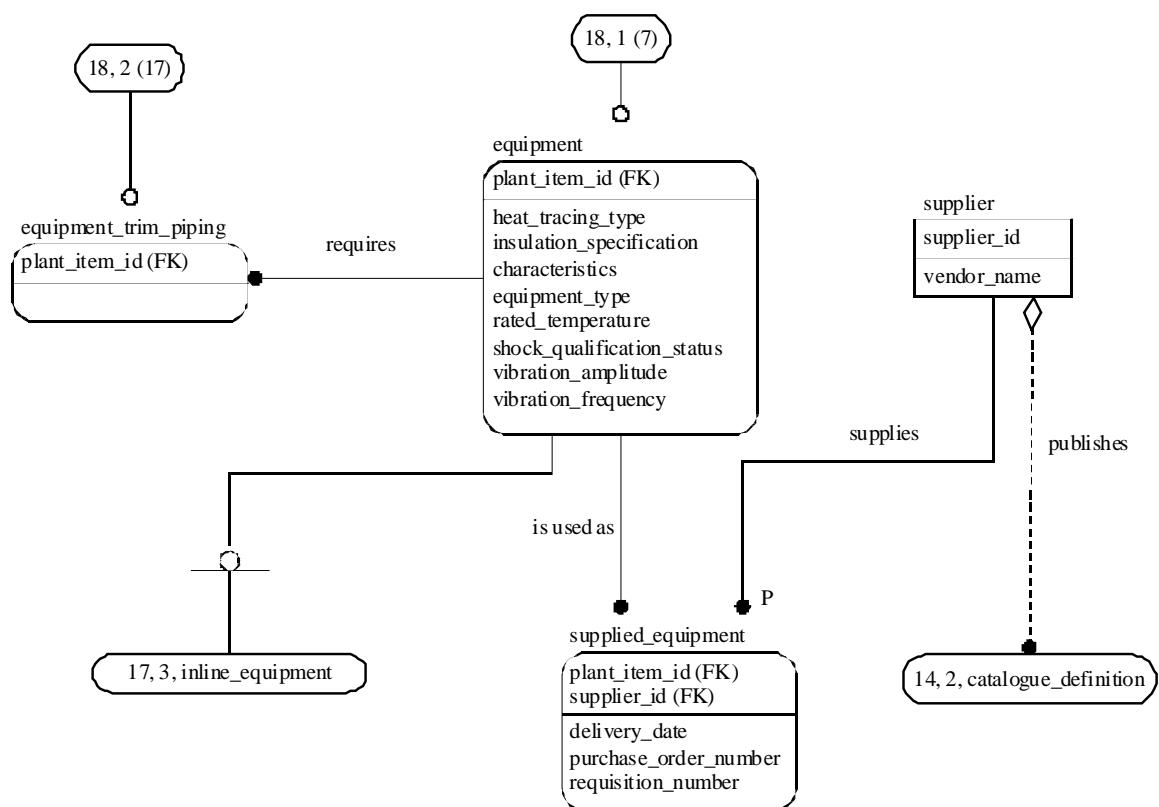


Figure G.19 - ARM diagram 18 of 42

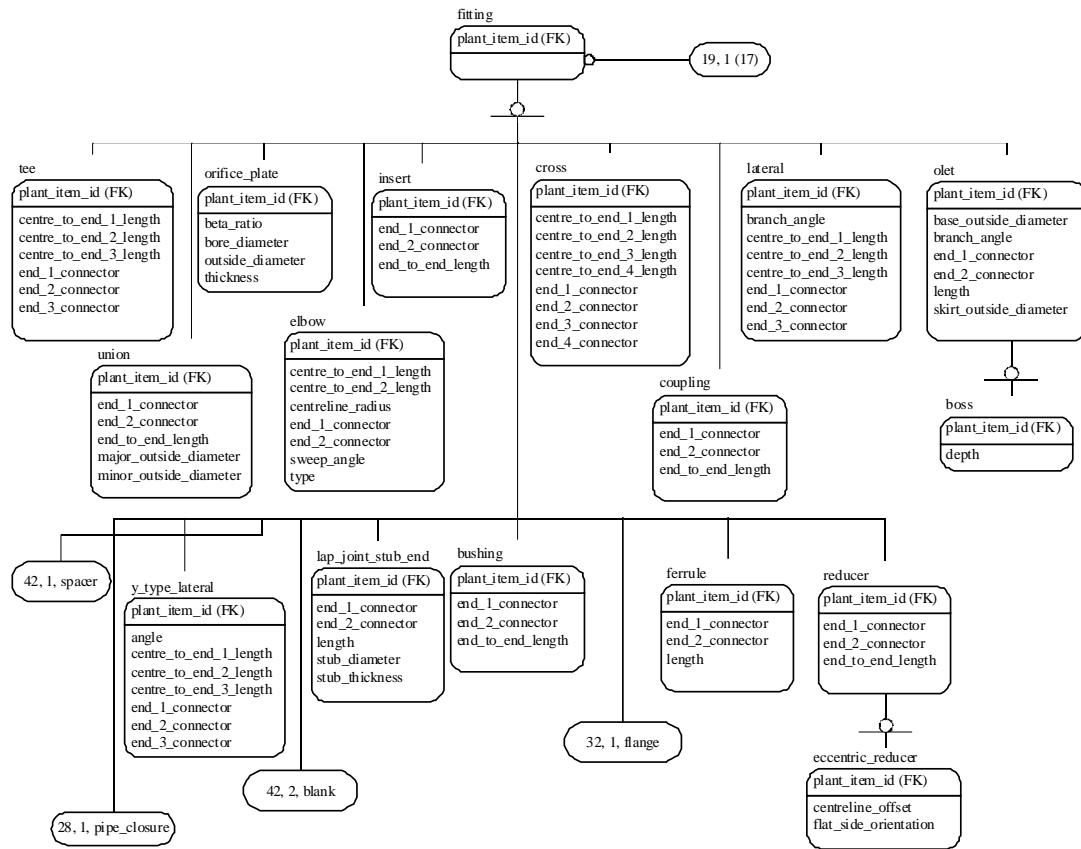


Figure G.20 - ARM diagram 19 of 42

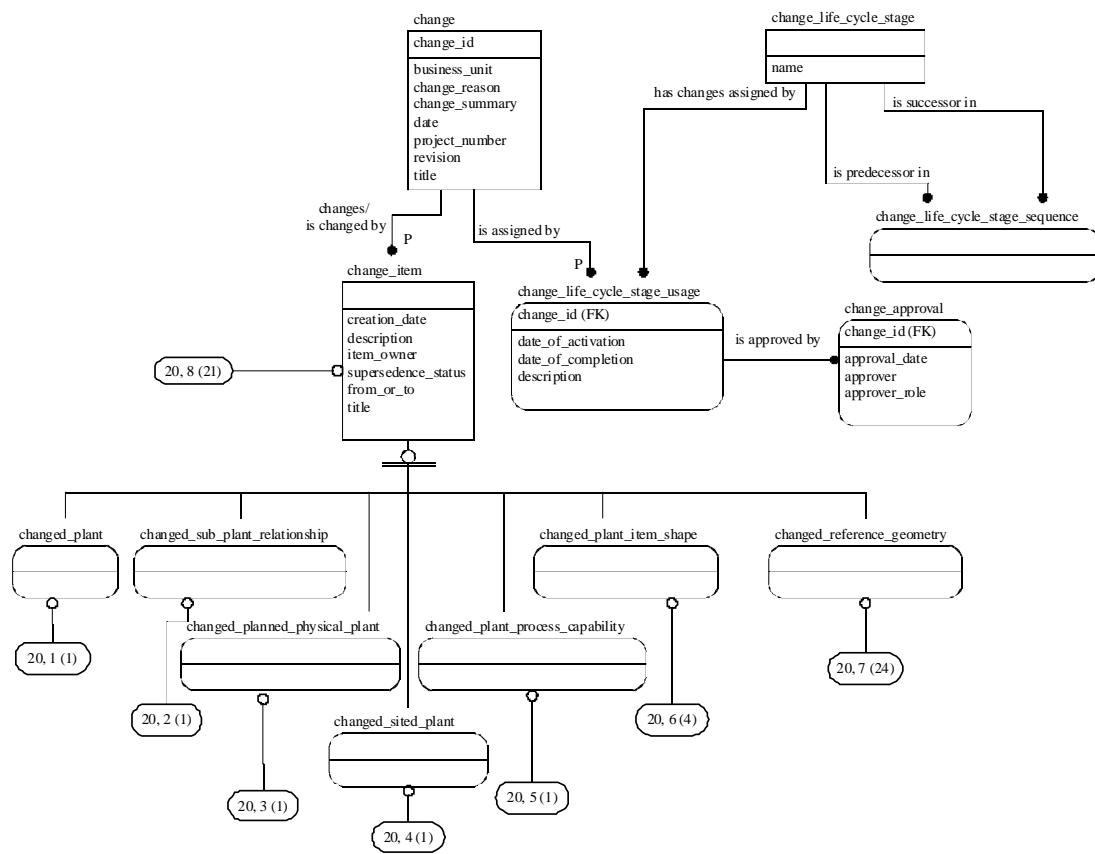


Figure G.21 - ARM diagram 20 of 42

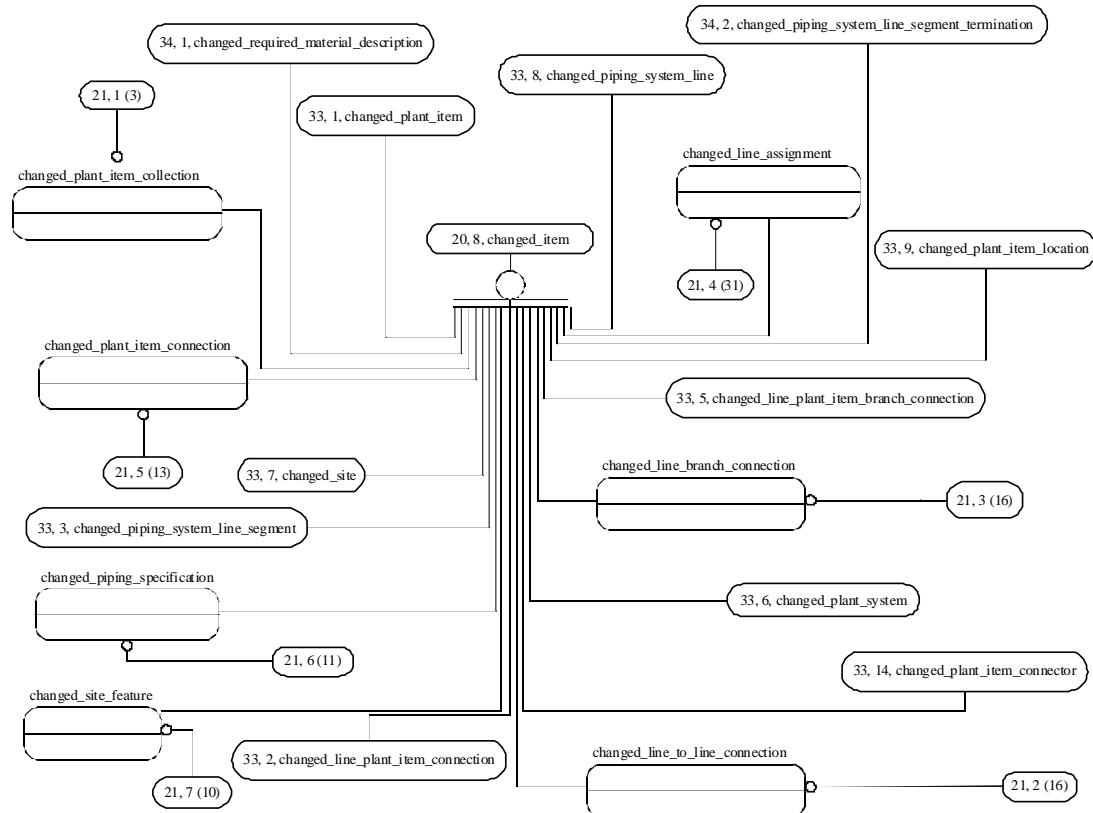


Figure G.22 - ARM diagram 21 of 42

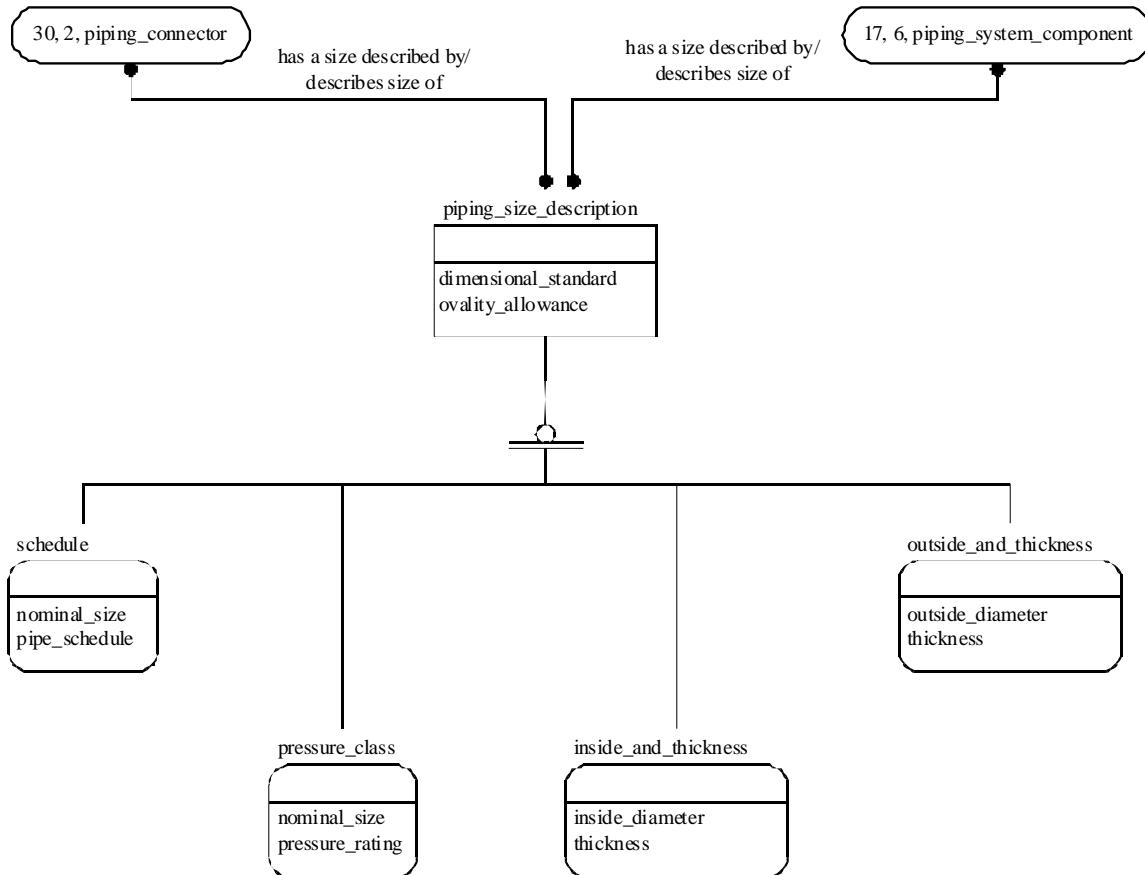


Figure G.23 - ARM diagram 22 of 42

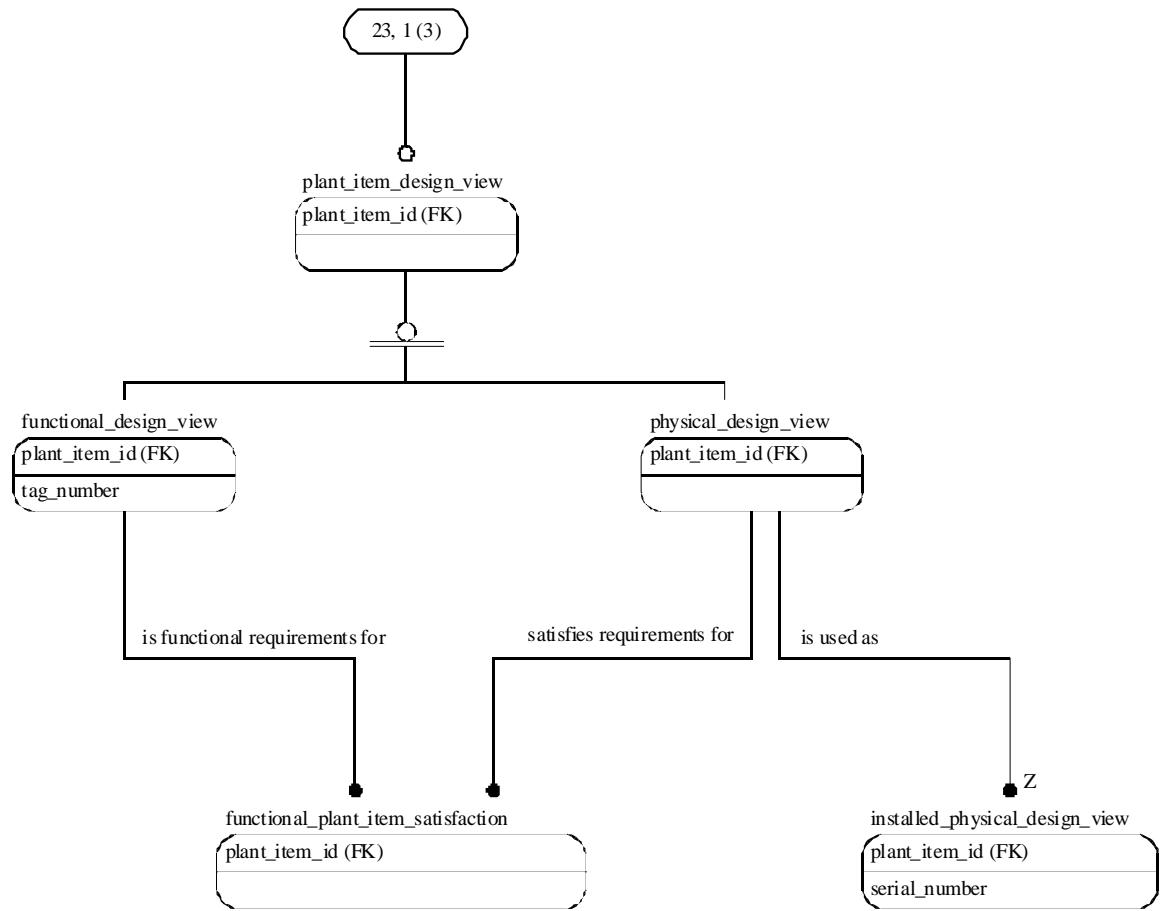


Figure G.24 - ARM diagram 23 of 42

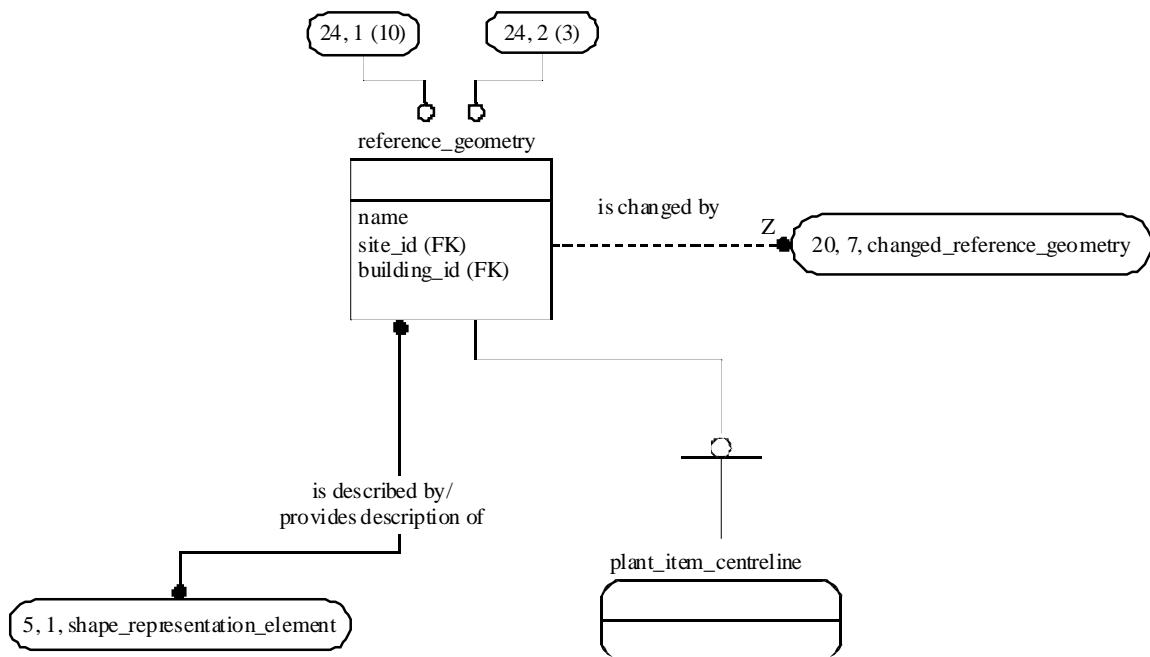


Figure G.25 - ARM diagram 24 of 42

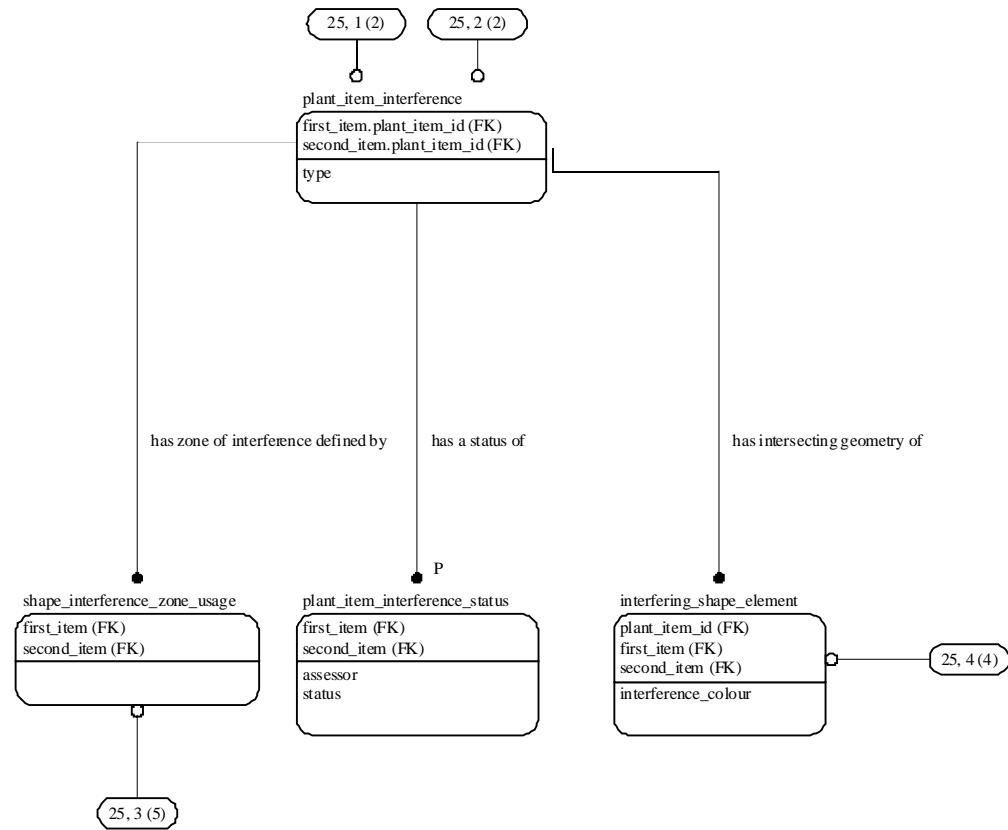


Figure G.26 - ARM diagram 25 of 42

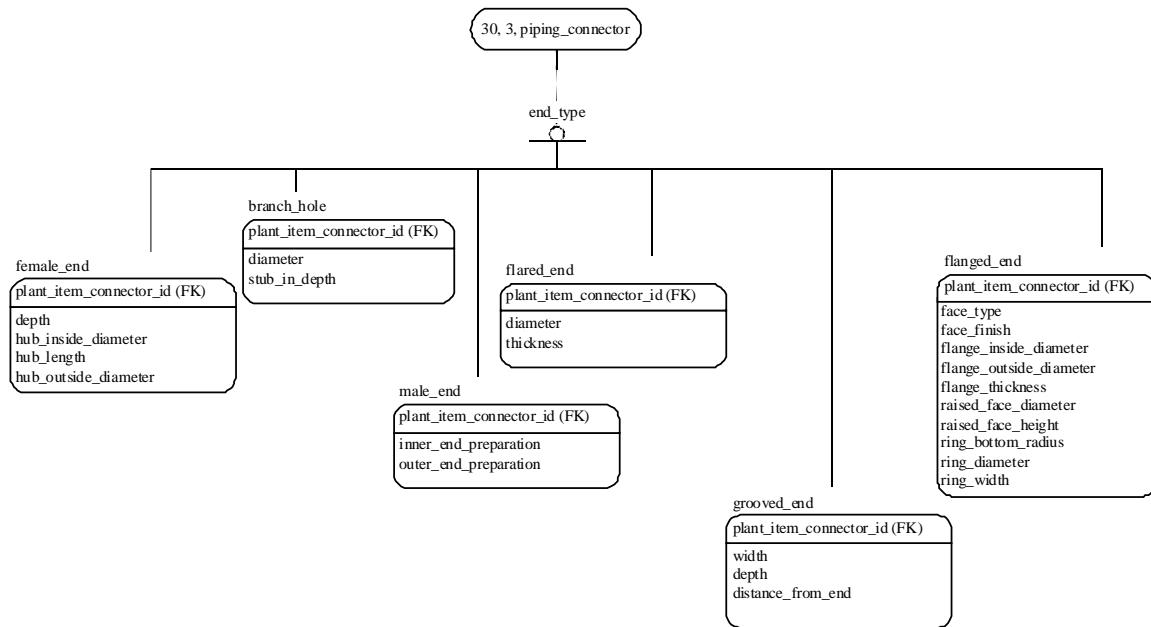


Figure G.27 - ARM diagram 26 of 42

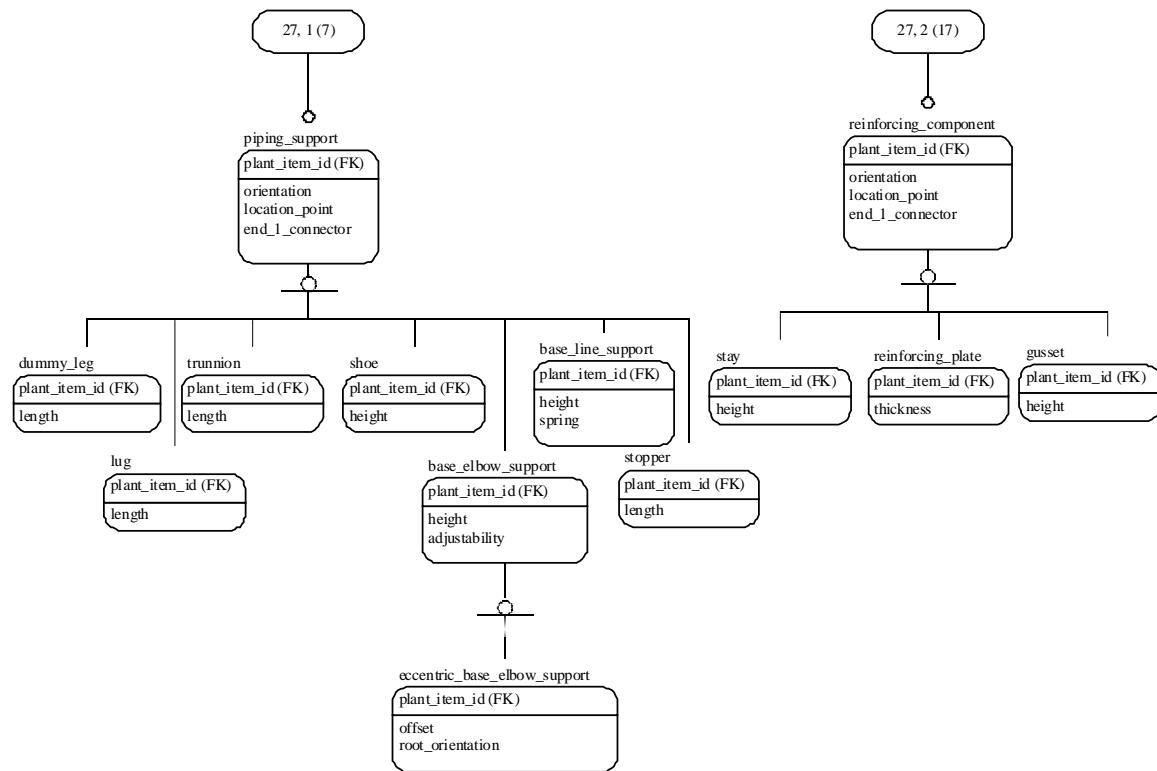


Figure G.28 - ARM diagram 27 of 42

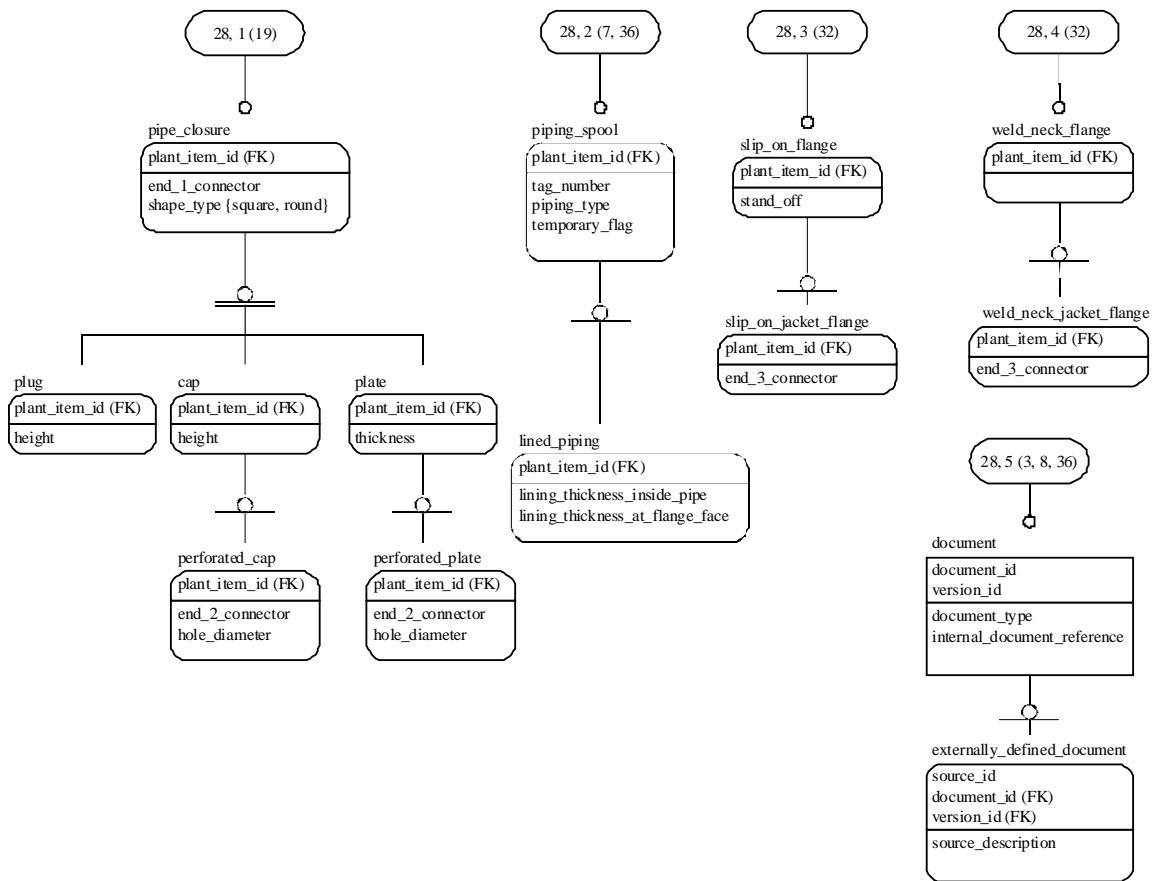


Figure G.29 - ARM diagram 28 of 42

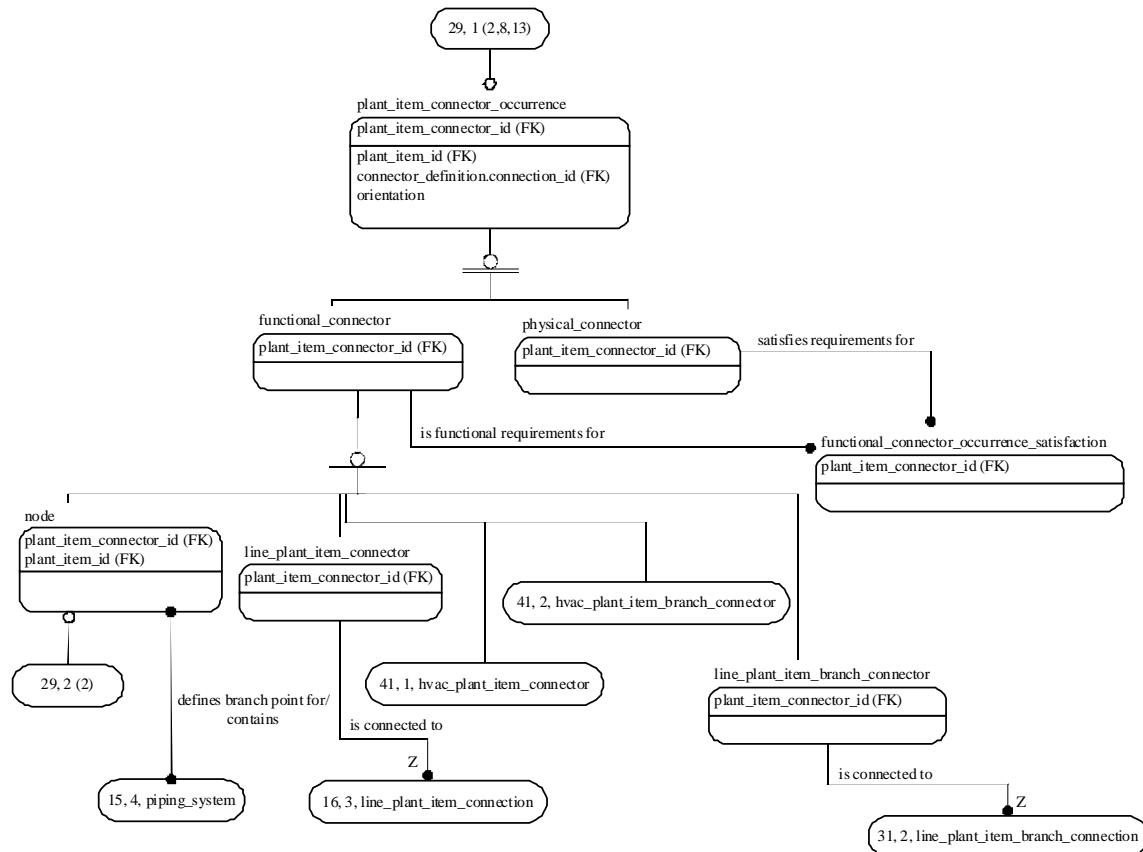


Figure G.30 - ARM diagram 29 of 42

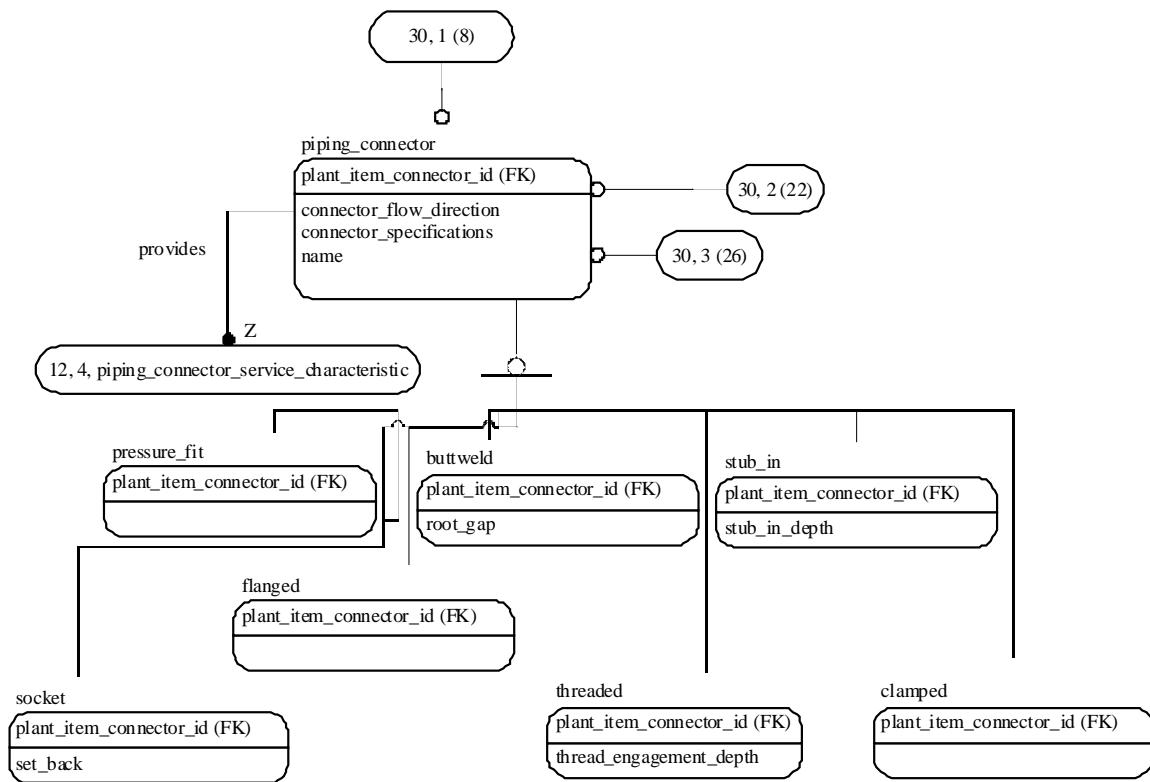


Figure G.31 - ARM diagram 30 or 42

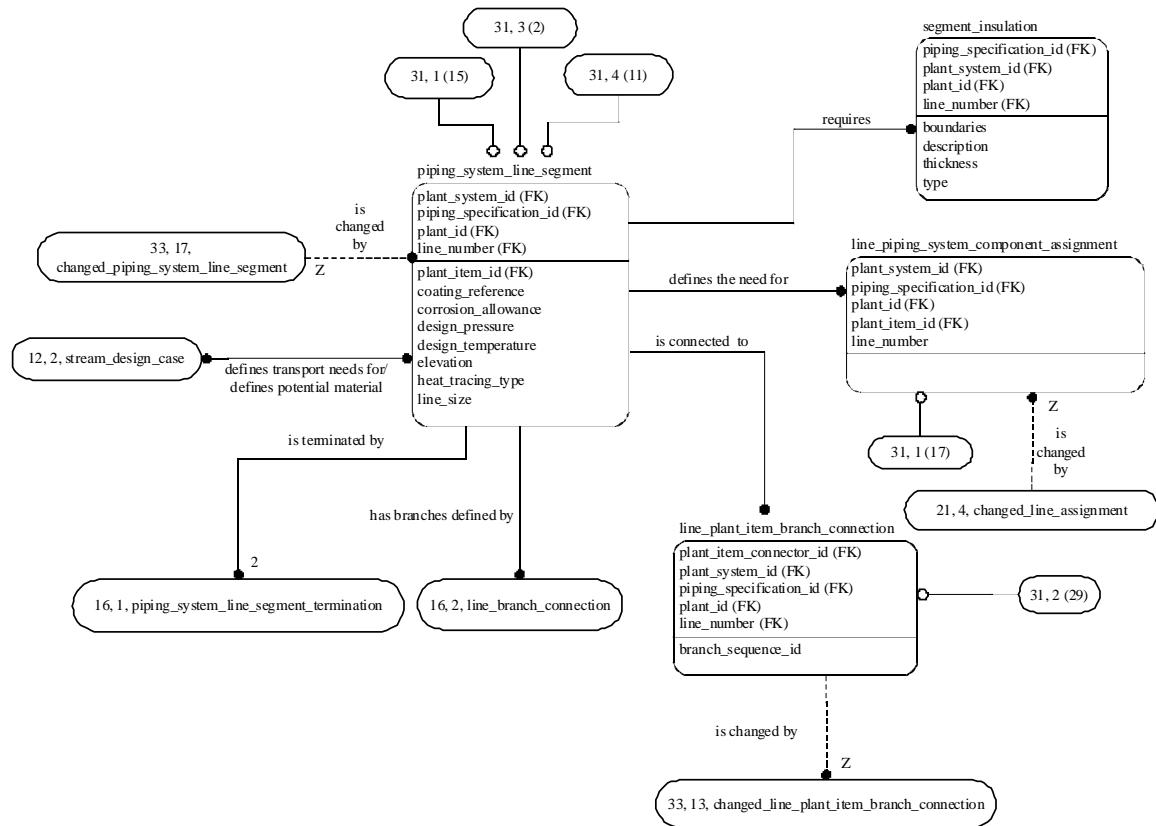


Figure G.32 - ARM diagram 31 of 42

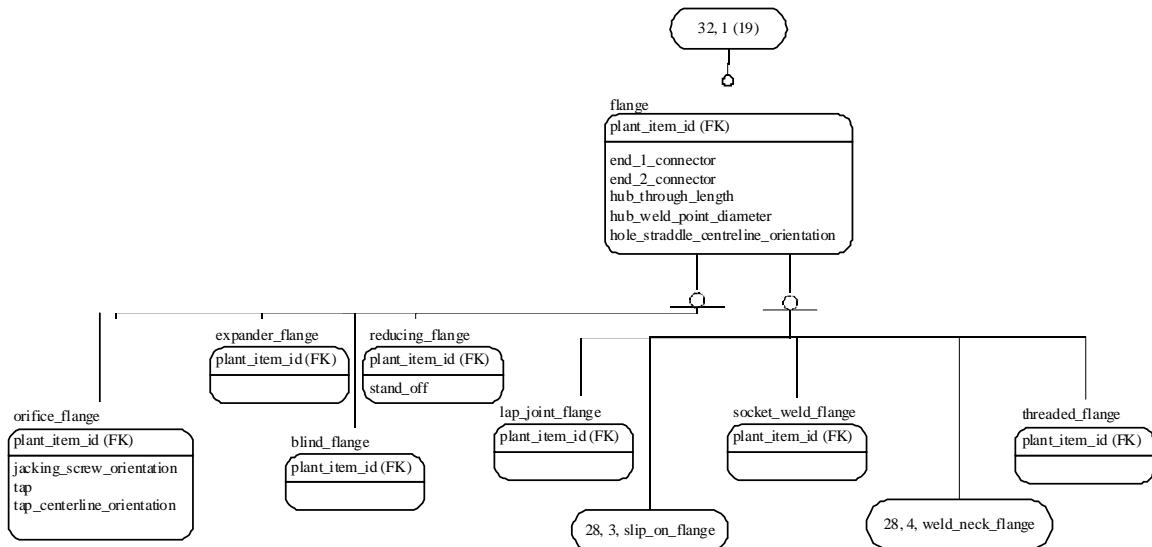


Figure G.33 - ARM diagram 32 of 42

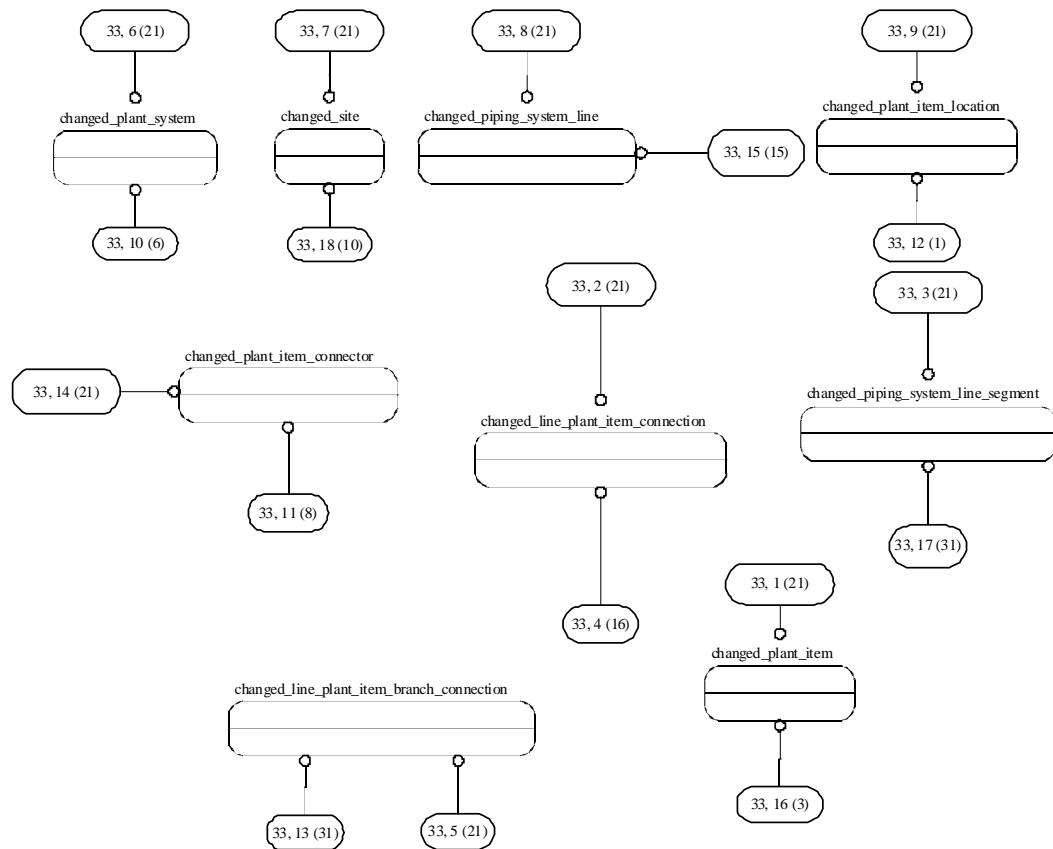


Figure G.34 - ARM diagram 33 of 42

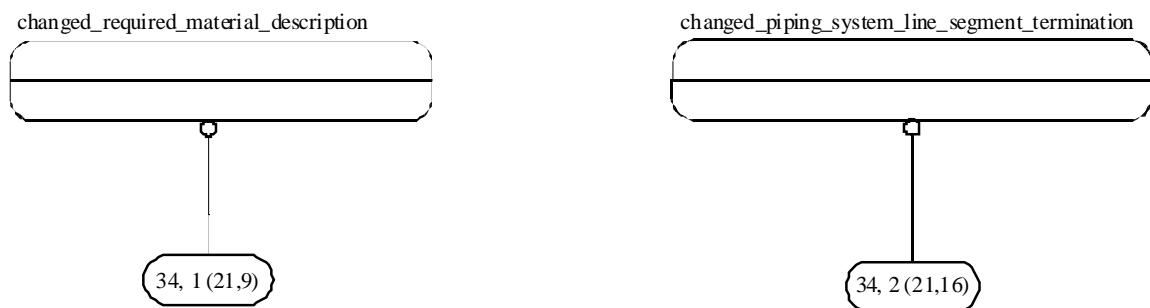


Figure G.35 - ARM diagram 34 of 42

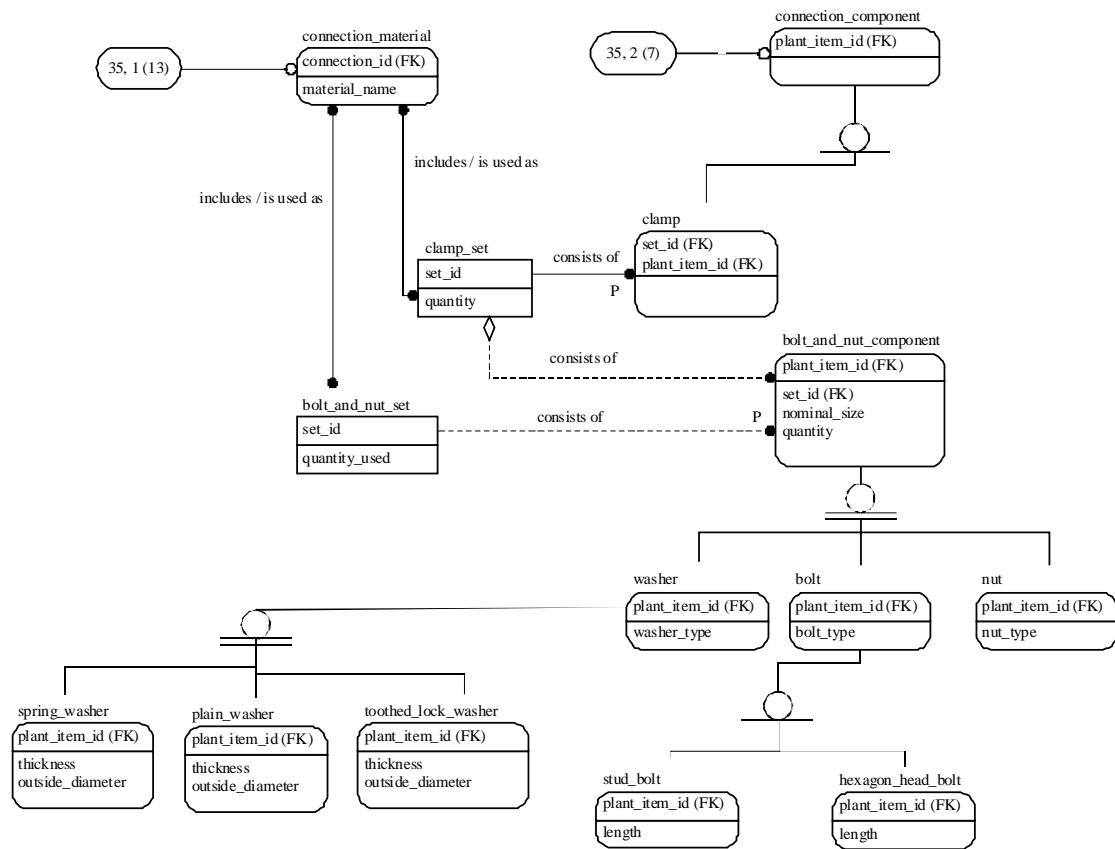


Figure G.36 - ARM diagram 35 of 42

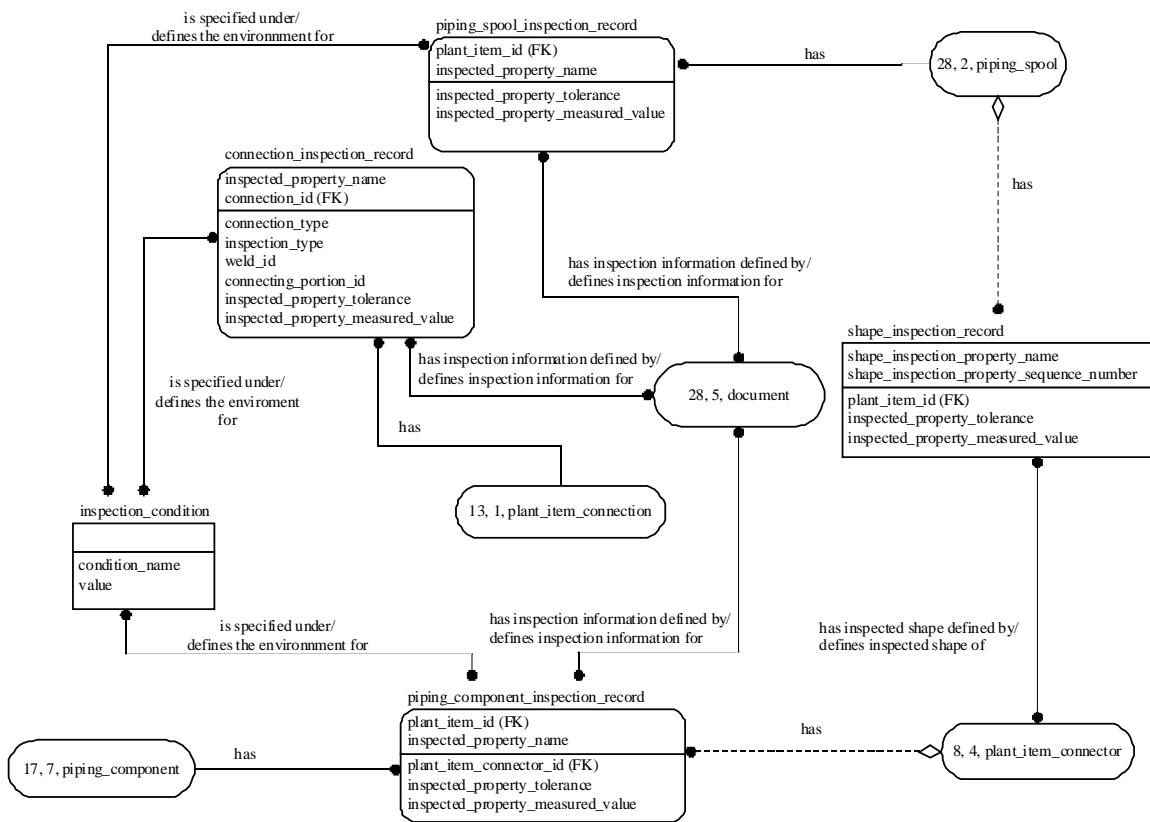


Figure G.37 - ARM diagram 36 of 42

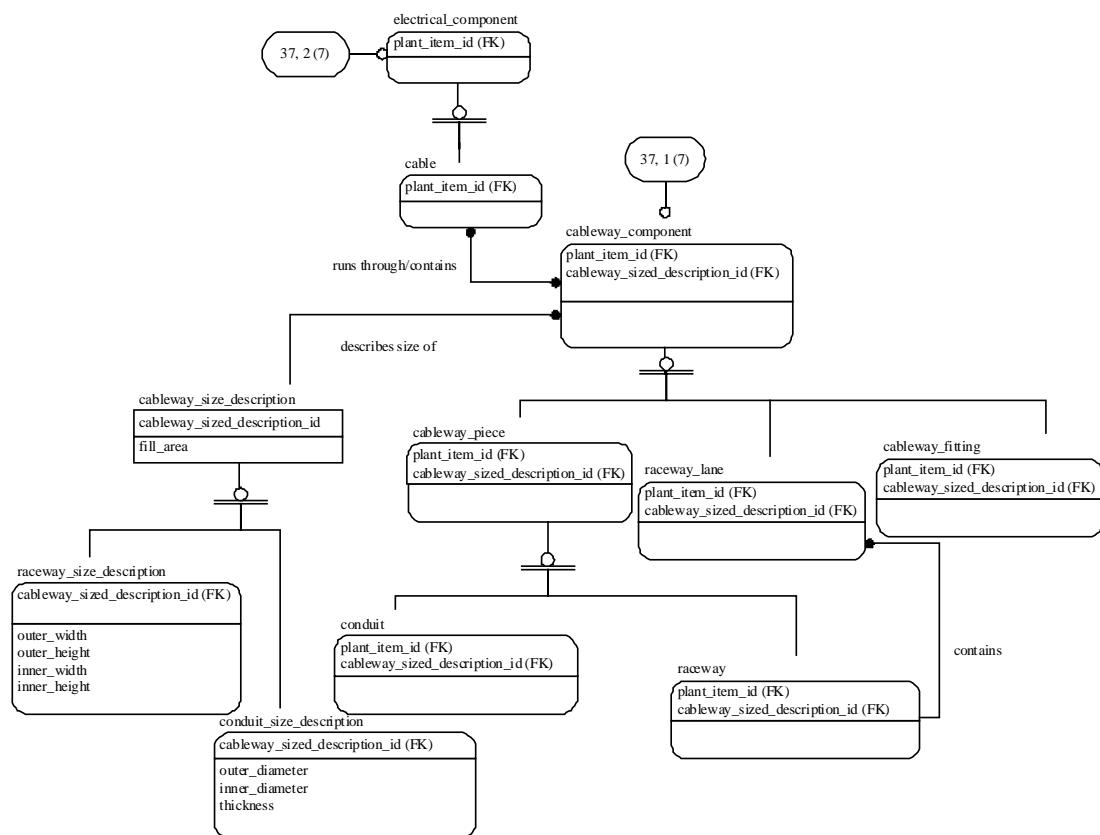


Figure G.38 - ARM diagram 37 of 42

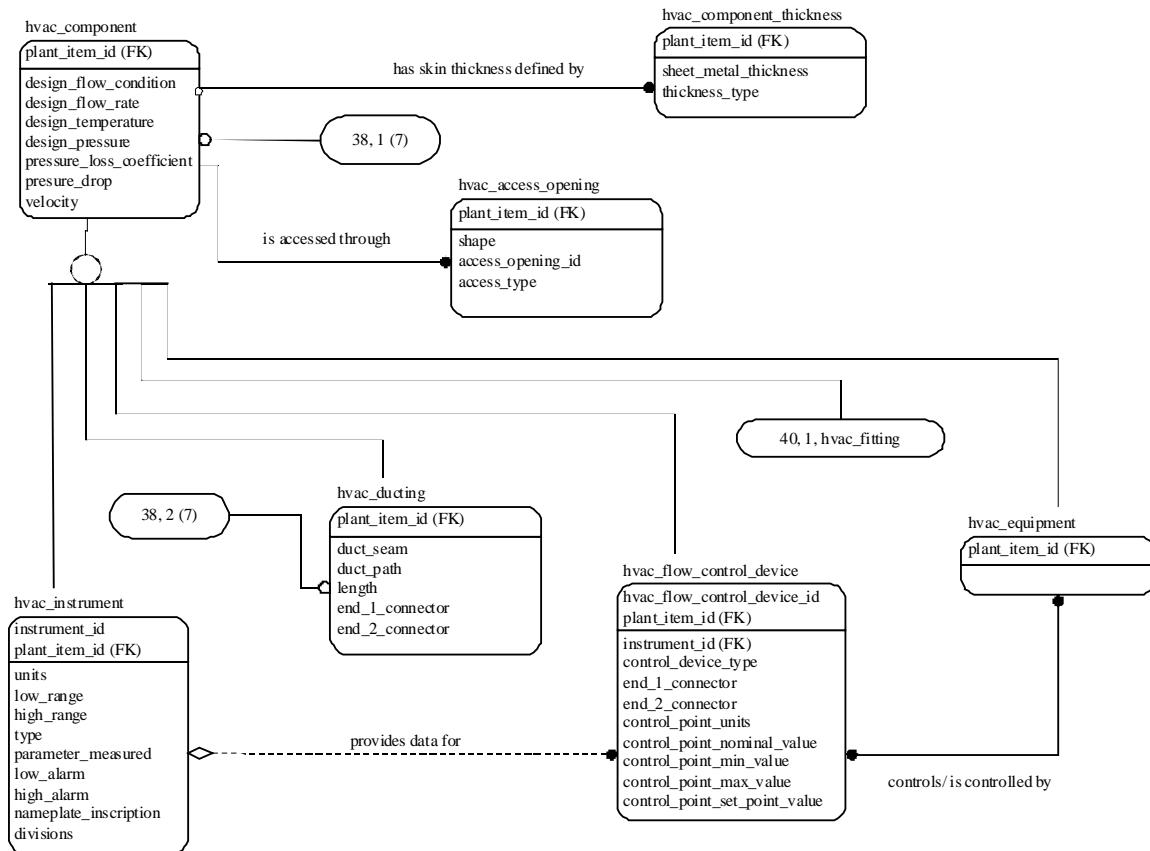


Figure G.39 - ARM diagram 38 of 42

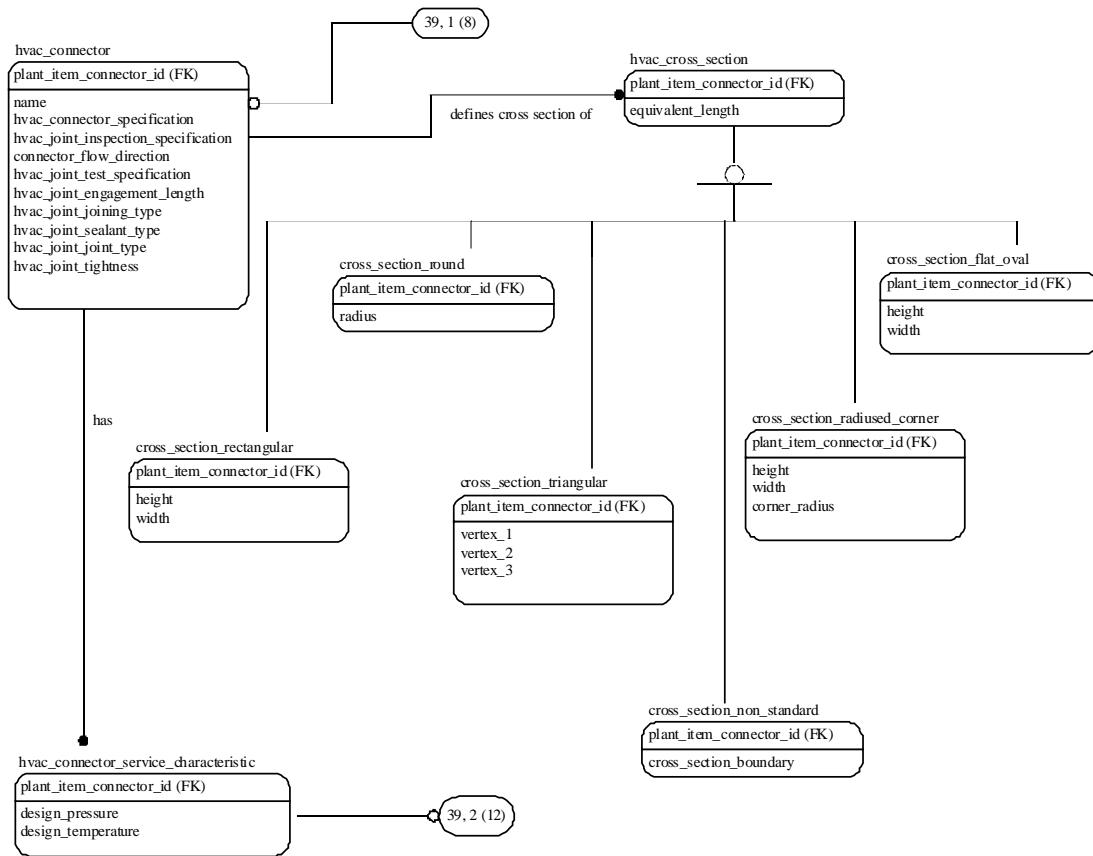


Figure G.40 - ARM diagram 39 of 42

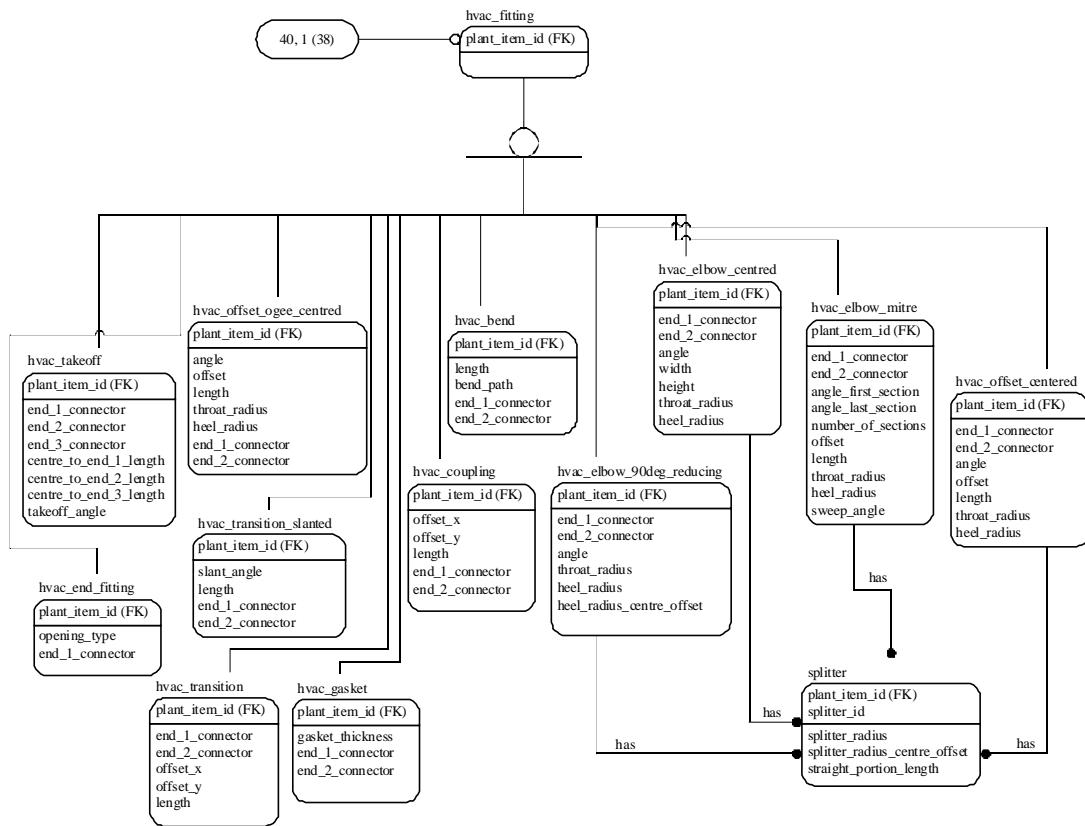


Figure G.41 - ARM diagram 40 of 42

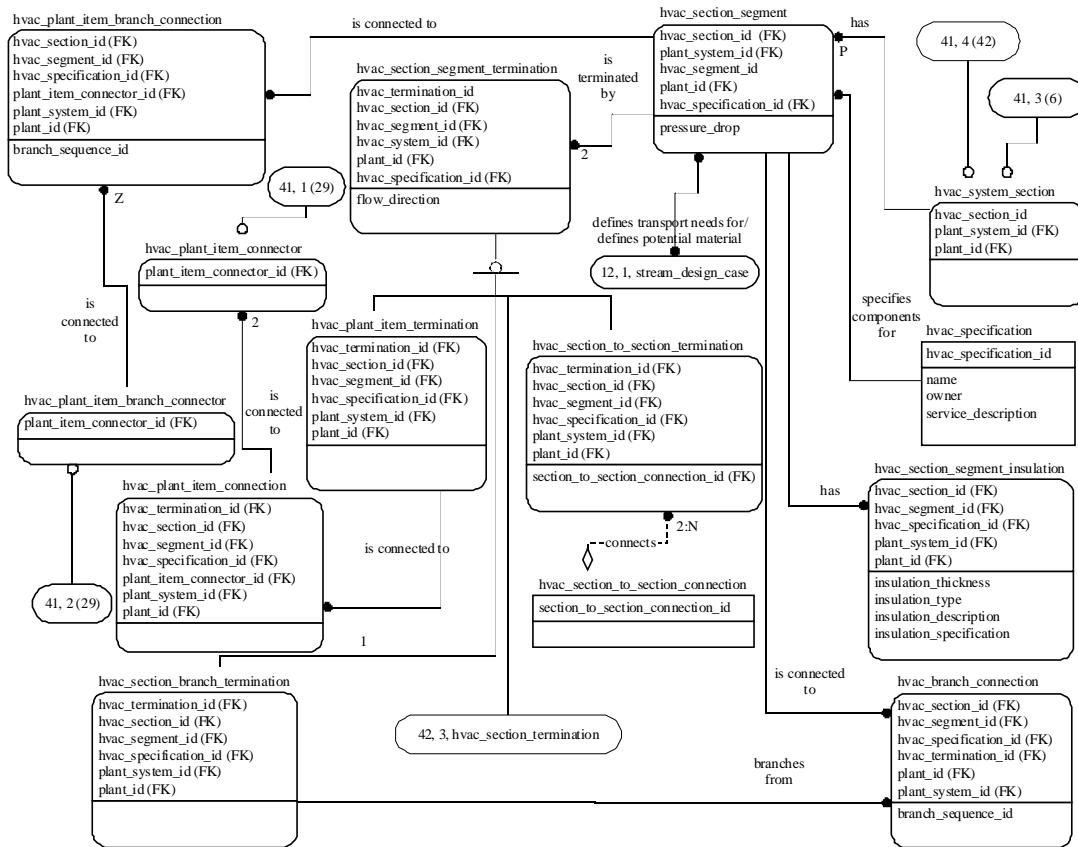


Figure G.42 - ARM diagram 41 of 42

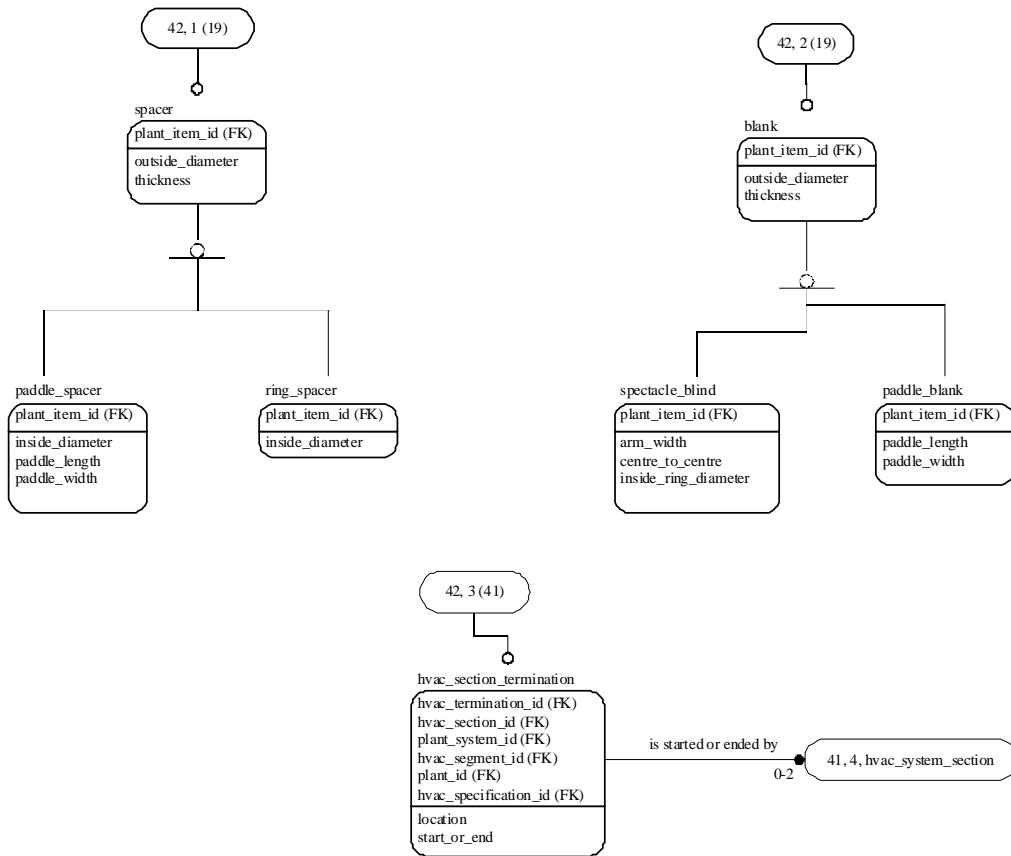


Figure G.43 - ARM diagram 42 of 42